



















# **Datasheet**

# **Ampire**

AM-19201080F1TZQW-A0

AM-10-007

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# 晶采光電科技股份有限公司 AMPIRE CO., LTD.

# SPECIFICATIONS FOR LCD MODULE

CUSTOMER	
CUSTOMER PART NO.	
AMPIRE PART NO.	AM-19201080F1TZQW-A0
APPROVED BY	
DATE	

- **□**Approved For Specifications
- □ Approved For Specifications & Sample

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#### RECORD OF REVISION

Revision Date	Page	Contents	Editor
2017/03/20		New Release	Mark
2018/01/25		Revise Electrical Specifications	Mark
2018/5/4 2018/6/19 2018/7/9	4 5 14	Update operating temp. Power Supply Current Update Pin 26 &27 define	Patrick Lawlite Lawlite

#### 1.0 General Descriptions

#### 1.1 Introduction

The LCM is a color active matrix TFT LCD module using amorphous silicon TFT's (Thin Film Transistors) as an active switching devices. This module has a 15.6 inch diagonally measured active area with FHD resolutions (1920 horizontal by 1080 vertical pixel array). Each pixel is divided into RED, GREEN, BLUE dots which are arranged in vertical Stripe and this module can display 16.7M colors. The TFT-LCD panel used for this module is a low reflection and higher color type.

#### 1.2 Features

Date: 2018/7/9

- 3.3 V Logic Power
- LVDS (2ch) Interface for 1920 RGB x 1080 resolution
- 16.7M Colors (6bit + HFRC)
- On board LED Driving circuit
- Green Product (RoHS)

#### 1.3 Product Summary

Items	Specifications	Unit
Screen Diagonal	15.6	Inch
Active Area	344.16 (H) ×193.59 (V)	mm
Pixel Format	1920 (H) x RGB x 1080 (V)	-
Pixel Pitch	0.17925 (H) X 0.17925 (V)	mm
Pixel Arrangement	R.G.B. Vertical Stripe	-
Display Mode	Normally Black	-
White Luminance	1000 (Тур)	cd /m2
Contrast Ratio	800 : 1 (Typ)	-
Input Voltage	3.3	V
Outline Dimensions	363.8x215.9Vx13.28	mm
Support Color	16.7M	-

#### 2.0 Absolute Maximum Ratings

ITEM	SYMBOL	VALU	JES	UNIT	REMARK	
I I EIVI	STIVIBUL	MIN	MAX	UNIT	KEWAKK	
Logic Signal Input Level	Vin	-0.3	+4.0	V		
Operation Temperature	T <sub>op</sub>	-30	75	$^{\circ}$ C		
Storage Temperature	T <sub>st</sub>	-30	80	$^{\circ}$		

Note (1) Permanent damage may occur to the LCD module if you operate beyond this specification. Functional operation should be restricted to the conditions which described under normal operating conditions.

Note (2) Ta =25±2°C

#### 3.0 ELECTRICAL SPECIFICATIONS

**Table 3 Electrical Specifications** 

Parameter		Min.	Тур.	Max.	Unit	Remarks
Power Supply Voltage	$V_{DD}$	3.0	3.3	3.6	٧	Note 1
Permissible Input Ripple Voltage	V <sub>RF</sub>	-	-	100	m∨	At V <sub>DD</sub> = 3.3V
Power Supply Current	I <sub>DD</sub>	-	1.2	-	Α	Note 1
Differential Input ∀oltage	V <sub>ID</sub>	200	-	600	m∨	

Notes: 1. The supply voltage is measured and specified at the interface connector of LCM.

The current draw and power consumption specified is for 3.3V at  $25^{\circ}$ C.

a) Typ : Mosaic Patternb) Max : R/G/B Pattern



# 4. Interface Timings

# **4.1 Timing Characteristics**

	Item	Symbols	Min	Тур	Max	Unit
	Frequency	1/Tc	100	141.4	160	MHz
Clock	High Time	Tch	-	4/7	-	Tc
	Low Time	Tcl	1	3/7	-	Tc
			1090	1100	1238	lines
Fra	ame Period	Tv	-	60	-	Hz
			1	16.7	-	ms
Vertical	Display Period	Tvd	1	1080	-	lines
One I	ine Scanning Period	Th	2080	2142	2400	clocks
Horiz	ontal Display Period	Thd	-	1920	-	clocks

Note\*: This Module can support low frame refresh rate 50Hz & 40Hz.

#### 4.2 Timing diagram

Date: 2018/7/9

AC Specifications (under normal operating conditions unless otherwise specified)

Figure 2-4 Mode Unstable Scilent Time

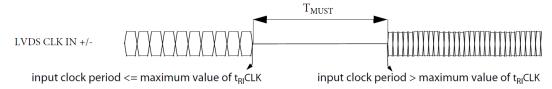
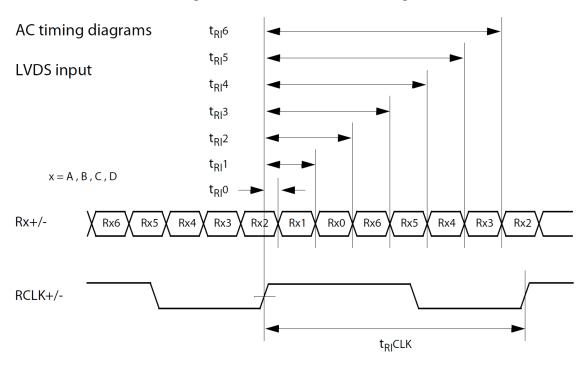


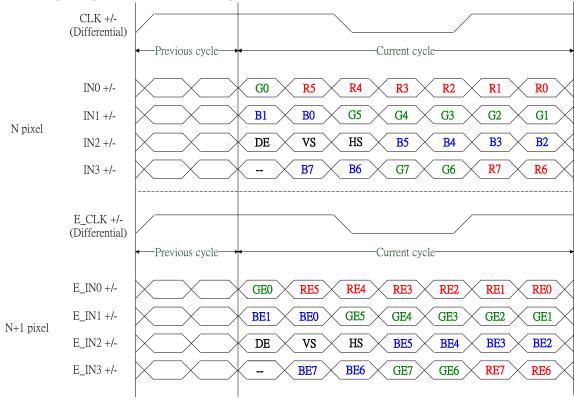
Figure 2-5 LVDS Receiver AC Timing



Symbol	Parameter	Min	Тур	Max	Units
F <sub>IC</sub>	Input LVDS Clock Frequency	25		85	MHz
T <sub>ICS</sub>	Input LVDS Clock Skew between any ports			1/5	T <sub>IC</sub>
T <sub>ICJ</sub>	Input LVDS Clock Jitter			2	ns
T <sub>must</sub>	Mode Unstable Silent Time	10			ms
t <sub>RI</sub> CLK	Input CLK period	11.8		40	ns
t <sub>RI</sub> 0	Input Data Position 0 ( t <sub>RI</sub> CLK = 11.8ns )	-0.3	0	+0.3	ns

Input Data Position 1 ( $t_{RI}CLK = 11.8ns$ )	TRICLK - 0.3	triCLK 7	$\frac{tRICLK}{7} + 0.3$	ns
Input Data Position 2 ( $t_{RI}CLK = 11.8ns$ )	$2\frac{tRICLK}{7} - 0.3$	2 <sup>triCLK</sup> 7	$2\frac{tRICLK}{7} + 0.3$	ns
Input Data Position 3 ( t <sub>RI</sub> CLK = 11.8ns )	$3\frac{tRICLK}{7} - 0.3$	3 triCLK 7	$3\frac{tRICLK}{7} + 0.3$	ns
Input Data Position 4 ( $t_{RI}CLK = 11.8ns$ )	4 triCLK 7 - 0.3	4 <sup>triCLK</sup>	$4\frac{\text{triCLK}}{7} + 0.3$	ns
Input Data Position 5 ( t <sub>RI</sub> CLK = 11.8ns )	5 triCLK - 0.3	5 triCLK 7	$5\frac{tRICLK}{7} + 0.3$	ns
Input Data Position 6 ( t <sub>RI</sub> CLK = 11.8ns )	6 TRICLK - 0.3	6 triCLK 7	$6\frac{tRICLK}{7} + 0.3$	ns
	Input Data Position 2 ( t <sub>RI</sub> CLK = 11.8ns )  Input Data Position 3 ( t <sub>RI</sub> CLK = 11.8ns )  Input Data Position 4 ( t <sub>RI</sub> CLK = 11.8ns )  Input Data Position 5 ( t <sub>RI</sub> CLK = 11.8ns )	Input Data Position 2 ( $t_{RI}$ CLK = 11.8ns) $2\frac{t_{RI}CLK}{7} - 0.3$ Input Data Position 3 ( $t_{RI}$ CLK = 11.8ns) $3\frac{t_{RI}CLK}{7} - 0.3$ Input Data Position 4 ( $t_{RI}$ CLK = 11.8ns) $4\frac{t_{RI}CLK}{7} - 0.3$ Input Data Position 5 ( $t_{RI}$ CLK = 11.8ns) $5\frac{t_{RI}CLK}{7} - 0.3$	Input Data Position 2 ( $t_{RI}$ CLK = 11.8ns) $2\frac{t_{RI}CLK}{7} - 0.3 \qquad 2\frac{t_{RI}CLK}{7}$ Input Data Position 3 ( $t_{RI}$ CLK = 11.8ns) $3\frac{t_{RI}CLK}{7} - 0.3 \qquad 3\frac{t_{RI}CLK}{7}$ Input Data Position 4 ( $t_{RI}$ CLK = 11.8ns) $4\frac{t_{RI}CLK}{7} - 0.3 \qquad 4\frac{t_{RI}CLK}{7}$ Input Data Position 5 ( $t_{RI}$ CLK = 11.8ns) $5\frac{t_{RI}CLK}{7} - 0.3 \qquad 5\frac{t_{RI}CLK}{7}$	Input Data Position 2 ( $t_{RI}$ CLK = 11.8ns) $2\frac{t_{RI}CLK}{7} - 0.3 \qquad 2\frac{t_{RI}CLK}{7} \qquad 2\frac{t_{RI}CLK}{7} + 0.3$ Input Data Position 3 ( $t_{RI}$ CLK = 11.8ns) $3\frac{t_{RI}CLK}{7} - 0.3 \qquad 3\frac{t_{RI}CLK}{7} \qquad 3\frac{t_{RI}CLK}{7} + 0.3$ Input Data Position 4 ( $t_{RI}$ CLK = 11.8ns) $4\frac{t_{RI}CLK}{7} - 0.3 \qquad 4\frac{t_{RI}CLK}{7} \qquad 4\frac{t_{RI}CLK}{7} + 0.3$ Input Data Position 5 ( $t_{RI}$ CLK = 11.8ns) $5\frac{t_{RI}CLK}{7} - 0.3 \qquad 5\frac{t_{RI}CLK}{7} \qquad 5\frac{t_{RI}CLK}{7} + 0.3$

#### 4.3 Timing Diagram of Interface Signal

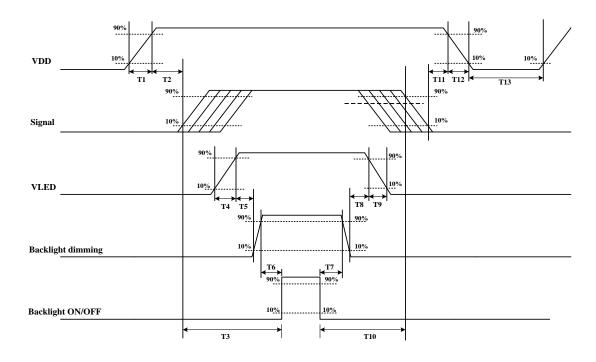


#### **4.4 Power Sequence**

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To prevent a latch-up or DC operation of the LCD module, the power on/off sequence shall be as shown below.

VDD power and LED on/off sequence are as follows. Interface signals are also shown in the chart. Signal shall be Hi-Z state or low level when VDD is off.



Domonoton		Value		Units
Parameter	Min.		Max.	Units
T1	0.5	Typ.	10	[ms]
T2	0	40	50	[ms]
T3	200	1	-	[ms]
T4	0.5	1	10	[ms]
T5	10	1	-	[ms]
T6	10	ı	-	[ms]
T7	0	ı	-	[ms]
T8	10	ı	-	[ms]
T9	-	ı	10	[ms]
T10	110	ı	-	[ms]
T11	0.5	16	50	[ms]
T12	-	-	100	[ms]
T13	1000	-	-	[ms]

#### **5.0 Optical Specifications**

Date: 2018/7/9

The optical characteristics are measured under stable conditions as following notes

Item	Conditio	ns	Min.	Тур.	Max.	Unit	Note
	Horizontal	$\theta_{L}$	80	85	-		
Viewing Angle	ПОПДОПІАІ	$\theta_{R}$	80	85	-	dograa	Note1
(CR>10)	Vertical	θτ	80	85	-	degree	Note
	verticai	$\theta_{B}$	80	85	-		
Contrast Ratio	Center	•	-	800	-	-	Note2
Response Time	Rising + Fa	alling	-	30	35	ms	Note5
	Red	Х		0.616		-	
	Red	у	Тур.	0.339	Typ. +0.05	-	Note3
	Green	Х		0.313		-	
Color Chromaticity	Green	у		0.582		-	
(CIE1931)	Blue	Х	-0.05	0.156		-	
	Blue	у		0.134		-	
	White	Х		0.313		-	
	White	у		0.329		-	
White Luminance	Center		800	1000	-	cd/m^2	Note4
Luminance Uniformity	9Points		75	-	-	%	Note4
Cross Talk	СТ	Θ=0	-	-	2.0	%	Note6

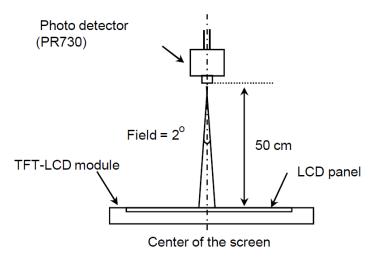
Notes 1: Viewing angle is the angle at which the contrast ratio is greater than 10.

The viewing angles are determined for the horizontal or 3, 9 o'clock direction and the vertical or 6, 12 o'clock direction with respect to the optical axis which is normal to the LCD surface(see Figure 1).

Notes 2: Contrast measurements shall be made at viewing angle of  $\Theta$ = 0 and at the center of the LCD surface. Luminance shall be measured with all pixels in the view field set first to white, then to the dark (black) state (see Figure 1). Luminance Contrast Ratio (CR) is defined mathematically as CR = Luminance when displaying a white raster / Luminance when displaying a black raster.

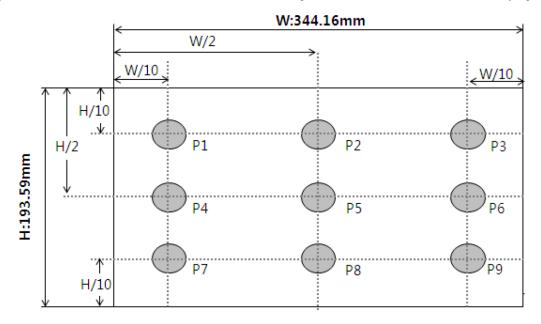
Notes 3: Reference only / Standard Front Surface Treatment Measured with green cover glass. The color chromaticity coordinates specified in Table 4 shall be calculated from the spectral data measured with all pixels first in red, green, blue and white. Measurements shall be made at the center of the panel.

Figure 1. Measurement Set Up



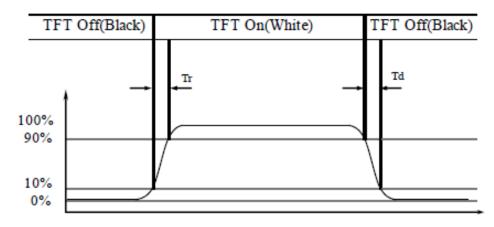
Optical characteristics measurement setup

Figure 2. White Luminance and Uniformity Measurement Locations (9 points)



Center Luminance of white is defined as luminance values of center 9 points across the LCD surface. Luminance shall be measured with all pixels in the view field set first to white. This measurement shall be taken at the locations shown in FIGURE 2 for a total of the measurements per display.

Figure 3. Response Time Testing

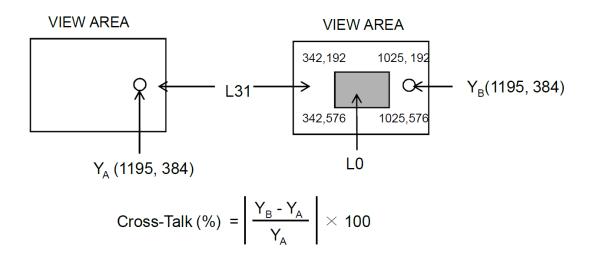


Note 5. The electro-optical response time measurements shall be made as Figure 4 by switching the "data" input signal ON and OFF. The times needed for the luminance to change from 10% to 90% is Tr, and 90% to 10% is Td.

#### Note 6.

Cross-Talk of one area of the LCD surface by another shall be measured by comparing the luminance (YA) of a 25mm diameter area, with all display pixels set to a gray level, to the luminance (YB) of that same area when any adjacent area is driven dark (Refer to Figure 4).

Figure 4. Cross Modulation Test Description



Where:

 ${
m Y_A}$  = Initial luminance of measured area (cd/m²)  ${
m Y_B}$  = Subsequent luminance of measured area (cd/m²)

The location measured will be exactly the same in both patterns

Cross-Talk of one area of the LCD surface by another shall be measured by comparing the luminance (YA) of a 25mm diameter area, with all display pixels set to a gray level, to the luminance (YB) of that same area when any adjacent area is driven dark (Refer to FIGURE 4).

# 6. Interface Connections

Pin#	Signal Name	Description
1	GND	Ground
2	NC	Not Connect
3	VDD	Power Supply
4	VDD	Power Supply
5	GND	Ground
6	GND	Ground
7	NC	Not Connect
8	NC	Not Connect
9	GND	Ground
10	INO-	-LVDS differential data input
11	IN0+	+LVDS differential data input
12	IN1-	-LVDS differential data input
13	IN1+	+LVDS differential data input
14	IN2-	-LVDS differential data input
15	IN2+	+LVDS differential data input
16	CLK-	-LVDS differential clock
17	CLK+	+LVDS differential clock
18	IN3-	-LVDS differential data input
19	IN3+	+LVDS differential data input
20	E_IN0-	-LVDS differential data input
21	E_IN0+	+LVDS differential data input
22	E_IN1-	-LVDS differential data input
23	E_IN1+	+LVDS differential data input
24	E_IN2-	-LVDS differential data input
25	E_IN2+	+LVDS differential data input
26	E_CLK-	-LVDS differential clock
27	E_CLK+	+LVDS differential clock
28	E_IN3-	-LVDS differential data input
29	E_IN3+	+LVDS differential data input
30	GND	Ground
31	GND	Ground
32	VLED	LED Power Supply
33	VLED	LED Power Supply

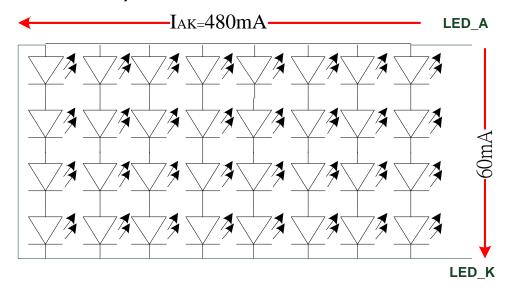
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34	VLED	LED Power Supply		
35	VLED	LED Power Supply		
36	LED_EN	LED Enable Pin:High→Enable		
37	LED_PWM	PWM Signal for LED Dimming Control		
38	GND	Ground		
39	GND	Ground		
40	GND	Ground		

### 7. LED Driving Conditions

ltom	Symbol	Values			l lmit	Nata
Item		Min.	Тур.	Max.	Unit	Note
LED Driver voltage	VLED	-	12	-	V	
Power Supply Current For LED Driver	ILED	-	960	-	mA	VLED=12V VADJ=5V (duty 100%)
ADJ Input Voltage	$V_{ADJ}$	-	5	VLED	V	duty=100%
ADJ Dimming Freq.	FadJ	0.1		30	kHz	
LED voltage	Vak		24	26.4	V	I <sub>AK</sub> =480mA Ta=25°C
LED ourrent	I <sub>AK</sub>		480		mA	Ta=25°C
LED current			360		mA	Ta=60°C
LED Life Time	-		50K		Hour	Note (2)

Note (1) The constant current source is needed for white LED back-light driving. When LCM is operated at 60 deg.C ambient temperature, the  $I_L$  of the LED back-light should be adjusted to 480mA max



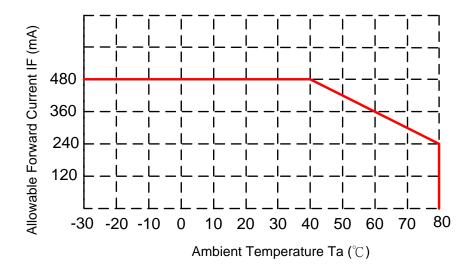
#### Note (2): Condition: Ta=25°C, continuous lighting

Life time is estimated data. Definitions of failure:

- 1. LCM brightness becomes half of the minimum value.
- 2. LED doesn't light normally.

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When LCM is operated over  $40^{\circ}$ C ambient temperature, the ILED should follow :



## 8. Reliability Test

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The reliability test items and its conditions are shown below.

Test Item	Test Conditions	Note
High Temperature Operation	75±3°C , t=240 hrs	
Low Temperature Operation	-30±3°C , t=240 hrs	
High Temperature Storage	80±3°C , t=240 hrs	1,2
Low Temperature Storage	-30±3°C , t=240 hrs	1,2
Storage at High Temperature and Humidity	50°C, 80% RH , 240 hrs	1,2
Thermal Shock Test	-20°C (30min) ~ 60°C (30min) , 100 cycles	1,2
Vibration Test (Packing)	Sweep frequency: 10~55~10 Hz/1min Amplitude: 0.75mm Test direction: X.Y.Z/3 axes Duration: 30 min/each axis	2

- Note (1) Condensation of water is not permitted on the module.
- Note (2) The module should be inspected after 1 hour storage in normal conditions (15-35°C, 45-65%RH).

#### 9. GENERAL PRECAUTION

#### 9.1 Use Restriction

This product is not authorized for use in life supporting systems, aircraft navigation control systems, military systems and any other application where performance failure could be life-threatening or otherwise catastrophic.

#### 9.2 Disassembling or Modification

Do not disassemble or modify the module. It may damage sensitive parts inside LCD module, and may cause scratches or dust on the display. AMPIRE does not warrant the module, if customers disassemble or modify the module.

#### 9.3 Breakage of LCD Panel

- (1) If LCD panel is broken and liquid crystal spills out, do not ingest or inhale liquid crystal, and do not contact liquid crystal with skin.
- (2) If liquid crystal contacts mouth or eyes, rinse out with water immediately.
- (3) If liquid crystal contacts skin or cloths, wash it off immediately with alcohol and rinse thoroughly with water.
- (4) Handle carefully with chips of glass that may cause injury, when the glass is broken.

#### 9.4 Electric Shock

Date: 2018/7/9

- (1) Disconnect power supply before handling LCD module.
- (2) Do not pull or fold the LED cable.
- (3) Do not touch the parts inside LCD modules and the fluorescent LED's connector or cables in order to prevent electric shock.

#### 9.5 Absolute Maximum Ratings and Power Protection Circuit

- (1) Do not exceed the absolute maximum rating values, such as the supply voltage variation, input voltage variation, variation in parts' parameters, environmental temperature, etc., otherwise LCD module may be damaged.
- (2) Please do not leave LCD module in the environment of high humidity and high temperature for a long time.
- (3) It's recommended to employ protection circuit for power supply.

#### 9.6 Operation

- (1) Do not touch, push or rub the polarizer with anything harder than HB pencil lead.
- (2) Use fingerstalls of soft gloves in order to keep clean display quality, when persons handle the LCD module for incoming inspection or assembly.
- (3) When the surface is dusty, please wipe gently with absorbent cotton or other soft material.
- (4) Wipe off saliva or water drops as soon as possible. If saliva or water drops contact with polarizer for a long time, they may cause deformation or color fading.
- (5) When cleaning the adhesives, please use absorbent cotton wetted with a little petroleum benzene or other adequate solvent.

#### 9.7 Mechanism

Please mount LCD module by using mounting holes arranged in four corners tightly.

#### 9.8 Static Electricity

- (1) Protection film must remove very slowly from the surface of LCD module to prevent from electrostatic occurrence.
- (2) Because LCD modules use CMOS-IC on circuit board and TFT-LCD panel, it is very weak to electrostatic discharge. Please be careful with electrostatic discharge. Persons who handle the module should be grounded through adequate methods.

#### 9.9 Strong Light Exposure

The module shall not be exposed under strong light such as direct sunlight. Otherwise, display characteristics may be changed.

#### 9.10 Disposal

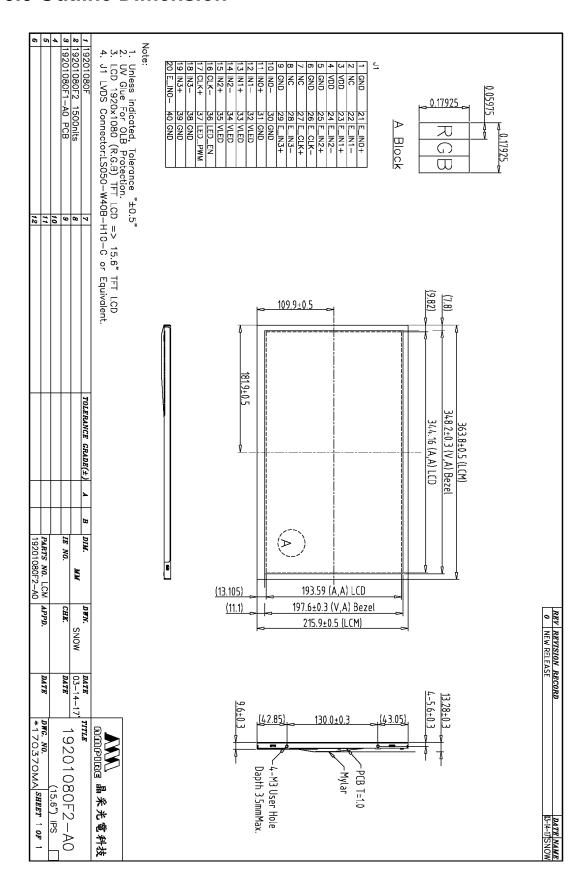
When disposing LCD module, obey the local environmental regulations.

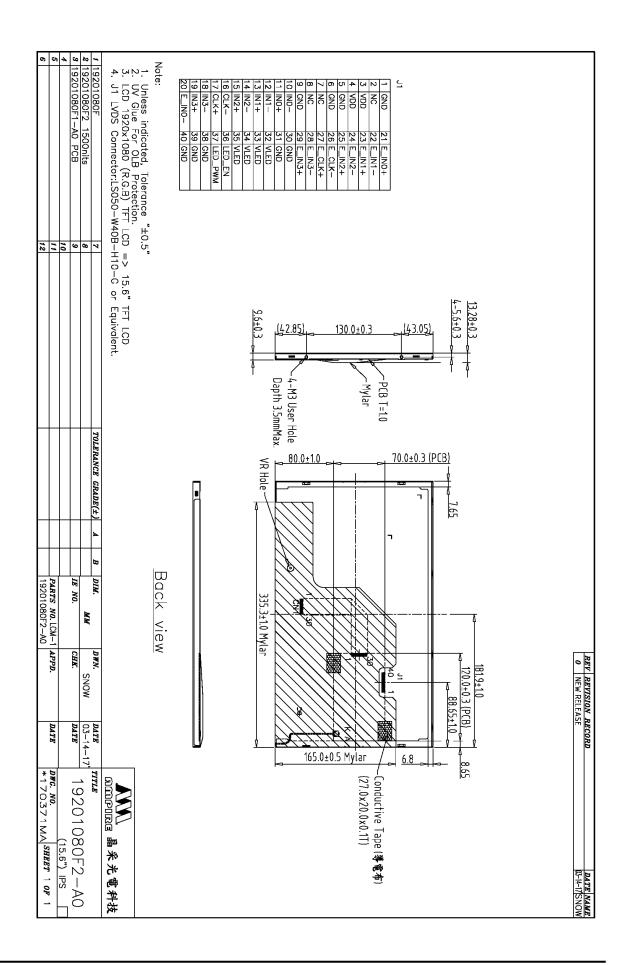
#### 9.11 Others

Date: 2018/7/9

Do not keep the LCD at the same display pattern continually. The residual image will happen and it will damage the LCD. Please use screen saver.

#### 10.0 Outline Dimension







Our company network supports you worldwide with offices in Germany, Austria, Switzerland, the UK and the USA. For more information please contact:

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