

Datasheet

LG Display

HD-10-099

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SPECIFICATION FOR APPROVAL

()	Preliminary Specification
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(lacktriangle) Promotion Specification

Title	11.6" TFT LCD			
BLIVED		SUPPLIER	LG Display Co. Ltd	

BUYER	
MODEL	

SUPPLIER	LG Display Co., Ltd.
MODEL	LP116WH6
Suffix	SLA1

	APPROVED BY	SIGNATURE
_		
-	/	

Please return 1 copy for your confirmation with your signature and comments.

APPROVED BY	SIGNATURE
REVIEWED BY	
PREPARED BY	
Product Engineeri LG Display Co	



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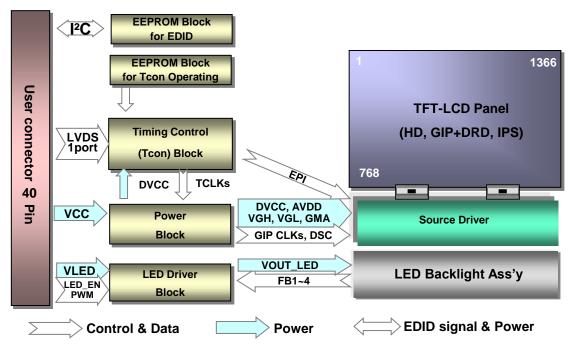
RECORD OF REVISIONS

Revision No	Revision Date	Page	Description	EDID ver
1.0	Aug. 12. 2013	-	Promotion specification Release	



1. General Description

The LP116WH6 is a Color Active Matrix Liquid Crystal Display with an integral LED backlight system. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive type display operating in the normally black mode. This TFT-LCD has 11.6 inches diagonally measured active display area with HD resolution (1366 horizontal by 768 vertical pixel array). Each pixel is divided into Red, Green and Blue subpixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 6-bit gray scale signal for each dot, thus, presenting a palette of more than 262,144 colors. The LP116WH6 has been designed to apply the interface method that enables low power, high speed, low EMI. The LP116WH6 is intended to support applications where thin thickness, low power are critical factors and graphic displays are important. In combination with the vertical arrangement of the subpixels, the LP116WH6 characteristics provide an excellent flat display for office automation products such as Notebook PC.



General Features

Active Screen Size	11.6 inches diagonal
Outline Dimension	270.0(H, Typ.) x 158.0(V, Typ.) x 3.1(D,Max) [mm]
Pixel Pitch	0.1875mm x 0.1875mm
Pixel Format	1366 horiz. By 768 vert. Pixels RGB strip arrangement
Color Depth	6-bit, 262,144 colors
Luminance, White	300 cd/m ² (Typ.5point)
Power Consumption	Total 3.86 Watt (Typ.) @ Mosaic Logic input 0.75Watt (Typ.), B/L input 3.1 Watt (Typ.)
Weight	200g (Max.)
Display Operating Mode	Transmissive mode, normally black
Surface Treatment	HC(3H) on Top , AG 25% on Bottom
RoHS Comply	Yes



2. Absolute Maximum Ratings

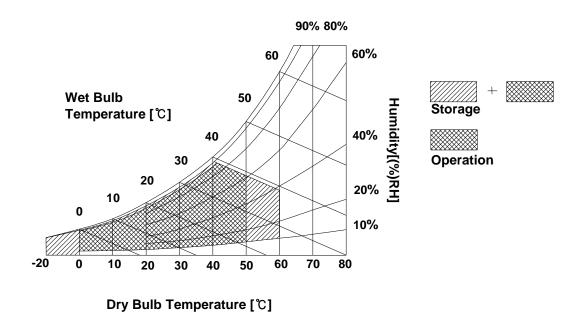
The following are maximum values which, if exceeded, may cause faulty operation or damage to the unit.

Table 1. ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Val	ues	Units	Notes	
Farameter	Symbol	Min	Max	Offics		
Power Input Voltage	VCC	-0.3	4.0	Vdc	at 25 ± 5°C	
Operating Temperature	Тор	0	50	°C	1	
Storage Temperature	Нѕт	-20	60	°C	1	
Operating Ambient Humidity	Нор	10	90	%RH	1	
Storage Humidity	Hst	10	90	%RH	1	

Note: 1. Temperature and relative humidity range are shown in the figure below.

Wet bulb temperature should be 39°C Max, and no condensation of water.





3. Electrical Specifications

3-1. Electrical Characteristics

The LP116WH6 requires two power inputs. The first logic is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The second backlight is the input about LED BL with LED Driver.

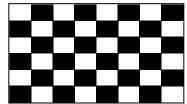
Table 2. ELECTRICAL CHARACTERISTICS

				Values			
Parameter	Symbol	Min	Тур	Max	Unit	Notes	
LOGIC :							
Power Supply Input Voltage		Vcc	3.0	3.3	3.6	V	1
Power Supply Input Current	Mosaic	Icc	-	227	261	mA	2
Power Consumption (Mosaic)	Pcc	-	0.75	0.86	W	3
Power Supply Inrush Current		ICC_P	-	-	1500	mA	4
LVDS Impedance		ZLVDS	90	100	110	Ω	5
BACKLIGHT : (with LED Drive	er)						
LED Power Input Voltage		VLED	7.0	12.0	21.0	V	6
LED Power Input Current		ILED	-	259	283	mA	7
LED Power Consumption		PLED	-	3.1	3.4	W	7
LED Power Inrush Current		ILED_P	-	-	1500	mA	8
PWM Duty Ratio			5	-	100	%	9
PWM Jitter		-	0	-	0.2	%	10
PWM Impedance		Zрwм	20	40	60	kΩ	
PWM Frequency		Fрwм	200	-	1000	Hz	11
PWM High Level Voltage		V _{PWM_H}	3.0	-	5.3	V	
PWM Low Level Voltage		V_{PWM_L}	0	-	0.3	V	
LED_EN Impedance		Zрwм	20	40	60	kΩ	
LED_EN High Voltage		VLED_EN_H	3.0	-	5.3	V	
LED_EN Low Voltage		VLED_EN_L	0	-	0.3	V	
Life Time	Life Time		15,000	-	-	Hrs	12
ESD at power off		Contact	'	± 8	•	kV	
		Air		± 15		kV	13
		User CNT		± 6		kV	

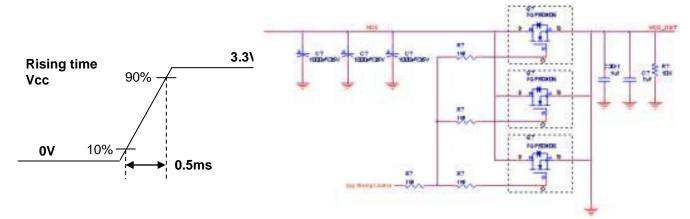


Note)

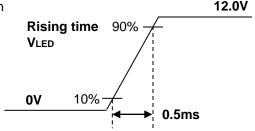
- 1. The measuring position is the connector of LCM and the test conditions are under 25 ℃, fv = 60Hz, Black pattern.
- 2. The specified Icc current and power consumption are under the Vcc = 3.3V, 25° C, fv = 60Hz condition and Mosaic pattern.



- This Spec. is the max load condition for the cable impedance designing.
- 4. The below figures are the measuring Vcc condition and the Vcc control block LGD used. The Vcc condition is same as the minimum of T1 at Power on sequence.



- 5. This impedance value is needed for proper display and measured form LVDS Tx to the mating connector.
- 6. The measuring position is the connector of LCM and the test conditions are under 25 °C.
- 7. The current and power consumption with LED Driver are under the Vled = 12.0V, 25 ℃, Dimming of Max luminance and White pattern with the normal frame frequency operated (60Hz).
- 8. The below figures are the measuring Vled condition and the Vled control block LGD used. VLED control block is same with Vcc control block.



- 9. The operation of LED Driver below minimum dimming ratio may cause flickering or reliability issue.
- 10. If Jitter of PWM is bigger than maximum, it may induce flickering.
- 11. This Spec. is not effective at 100% dimming ratio as an exception because it has DC level equivalent to 0Hz. In spite of acceptable range as defined, the PWM Frequency should be fixed and stable for more consistent brightness control at any specific level desired.
- 12. The life time is determined as the time at which brightness of LCD is 50% compare to that of minimum value specified in table 7. under general user condition.
- 13. LGD ESD point is case-top, front of panel, bottom cover and user CNT.



3-2. Interface Connections

This LCD employs two interface connections, a 40 pin connector used for the module electronics interface and the other connector used for the integral backlight system.

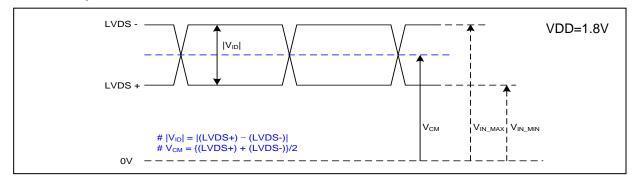
Table 3. MODULE CONNECTOR PIN CONFIGURATION (CN1)

Pin	Symbol	Description	Notes
1	NC	No Connection	
2	VCC	LCD Logic and driver power (3.3V Typ.)	
3	VCC	LCD Logic and driver power (3.3V Typ.)	
4	V EEDID	DDC Power (3.3V)	
5	NC	No Connection	
6	Clk EEDID	DDC Clock	1, Interface chips
7	DATA EEDID	DDC Data	1.1 LCD: TLI, TL2356EP(LCD Controller)
8	ORX0-	Negative LVDS differential data input	Including LVDS Receiver.
9	ORX0+	Positive LVDS differential data input	1.2 System : SiW LVDSRx or equivalent
10	GND	LCM Ground	or equivalent * Pin to Pin compatible with LVDS
11	ORX1-	Negative LVDS differential data input	1 III to 1 III compatible with EVDS
12	ORX1+	Positive LVDS differential data input	2. Connector
13	GND	LCM Ground	2.1 LCD: IS050-L40B-C10, UJU or equivalent
14	ORX2-	Negative LVDS differential data input	2.2 Mating: 20453-040T-0x, I-PEX or equivalent 2.3 Connector pin arrangement
15	ORX2+	Positive LVDS differential data input	2.3 Connector pin arrangement
16	GND	LCM Ground	_
1	ORXC-	Negative LVDS differential clock input	40 1
17	ORXC+	Positive LVDS differential clock input	
18	GND	LCM Ground	
19	NC	No Connection	[LCD Module Rear View]
20			[LCD Module Real Mew]
21	NC	No Connection	
22	GND	LCM Ground	
23	NC NC	No Connection	
24	NC	No Connection	
25	GND	LCM Ground	
26	NC	No Connection	
27	NC	No Connection	
28	GND	LCM Ground	
29	NC NC	No Connection	
30	NC	No Connection	
31	GND	LCM Ground (LED Backlight Ground)	
32	GND	LCM Ground (LED Backlight Ground)	
33	GND	LCM Ground (LED Backlight Ground)	
34	NC	No Connection	
35	PWM	System PWM Signal input for dimming	
36	LED_EN	LED Backlight On/Off	
37	NC	No Connection	
38	VLED	LED Backlight Power (7V-21V)	
39	VLED	LED Backlight Power (7V-21V)	
40	VLED	LED Backlight Power (7V-21V)	
40	VLED	LED Backlight Power (7V-21V)	



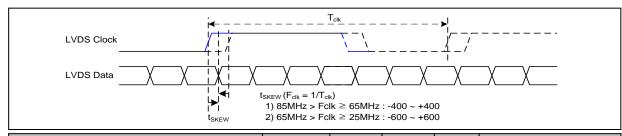
3-3. LVDS Signal Timing Specifications

3-3-1. DC Specification



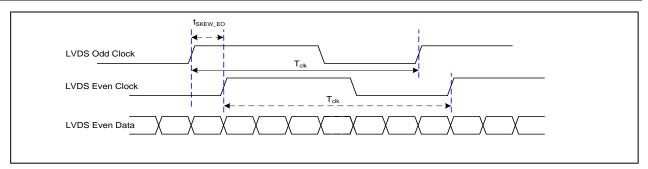
Description	Symbol	Min	Тур	Max	Unit	Notes
LVDS Differential Voltage	V _{ID}	100	-	600	mV	-
LVDS Common mode Voltage	V_{CM}	V _{ID} /2	1.2	VDD- V _{ID} /2	V	-
LVDS Input Voltage Range	V _{IN}	0.3	-	VDD	V	-

3-3-2. AC Specification

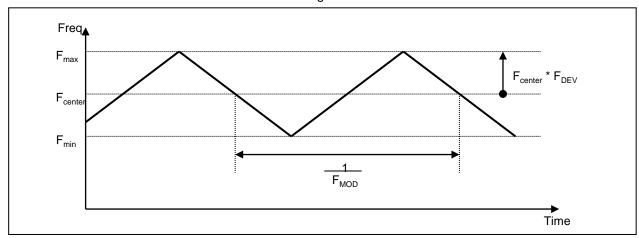


Description	Symbol	Min	Max	Unit	Notes
LVDS Clock to Data Skow Margin	t _{SKEW}	- 400	+ 400	ps	85MHz > Fclk ≥ 65MHz
LVDS Clock to Data Skew Margin	t _{SKEW}	- 600	+ 600	ps	65MHz > Fclk ≥ 25MHz
LVDS Clock to Clock Skew Margin (Even to Odd)	t _{SKEW_EO}	- 1/7	+ 1/7	T _{clk}	-
Maximum deviation of input clock frequency during SSC	F _{DEV}	-	± 3	%	-
Maximum modulation frequency of input clock during SSC	F _{MOD}	-	200	KHz	-





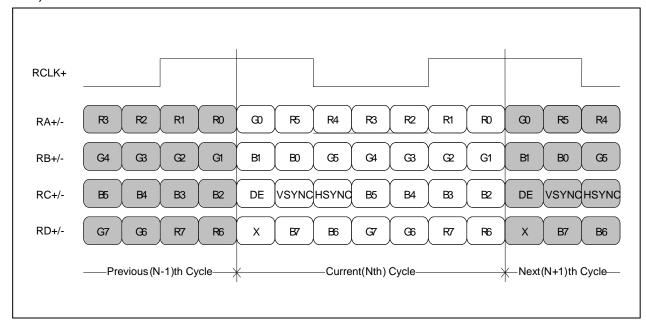
< Clock skew margin between channel >



< Spread Spectrum >

3-3-3. Data Format

1) LVDS 1 Port



< LVDS Data Format >



3-4. Signal Timing Specifications

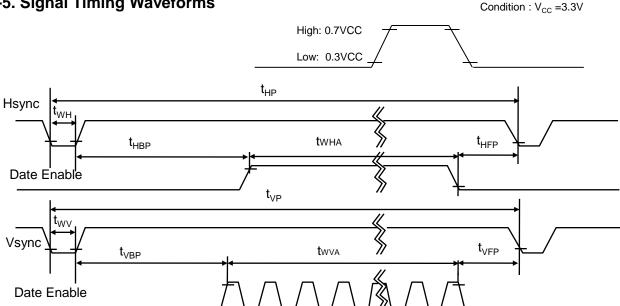
This is the signal timing required at the input of the User connector. All of the interface signal timing should be satisfied with the following specifications and specifications of LVDS Tx/Rx for its proper operation.

Table 4. TIMING TABLE

ITEM	Symbol		Min.	Тур.	Max.	Unit	Note
DCLK	Frequency	f _{CLK}	-	70.0	-	MHz	
	Period	t _{HP}	-	1492	-		
Hsync	Width	t _{wH}	-	48	-	t CLK	
	Width-Active	t w _{HA}	-	1366	-		
	Period	t _{VP}	-	782	-		
Vsync	Width	t _{wv}	-	5	-	tHP	
	Width-Active	tw _{VA}	-	768	-		
	Horizontal back porch	t _{HBP}	-	42	-	+01.14	
Data	Horizontal front porch	t _{HFP}	-	36	-	tCLK	
Enable	Vertical back porch	t _{VBP}	-	6	-	HID	
	Vertical front porch	t _{VFP}	-	3	=	tHP	

Appendix) All reliabilities are specified for timing specification based on refresh rate of 60 Hz. Even though actual performance in 50Hz and 40Hz for low power is displayed normally, remark and inform to user that display quality in 40 Hz and 50 Hz is out of guarantee range.

3-5. Signal Timing Waveforms





3-6. Color Input Data Reference

The brightness of each primary color (red,green and blue) is based on the 6-bit gray scale data input for the color; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

Table 5. COLOR DATA REFERENCE

									Inp	out Co	olor D	ata							
	Color			RE	ΞD					GRE	EEN					BL	UE		
	70.0.	MSE	3				LSB	_						MSE					LSB
	r	R 5	R 4	R 3	R 2	R 1	R 0	G 5	G 4	G 3	G 2	G 1	G 0	B 5	B 4	B 3	B 2	B 1	B 0
	Black	0	0			0	0	0	0	0	0		0	0				0	0
	Red	1			1	1	1	0	0	0	0		0	0	0		0	0	0
	Green	0	0		0	0	0	1	1	1			1	0	0		0	0	0
Basic	Blue	0	0	0		0	0	0	0	0	0	0	0	1	1	.1	1	1	
Color	Cyan	0	0	0		0	0	1	1	1		. 1	1	1	. 1	.1	1		1
	Magenta	1	1	1	. 1	1		0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	1	1	1	1	1		1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	RED (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (01)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
RED		ļ																	
	RED (62)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (01)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
GREEN					 														
	GREEN (62)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	GREEN (63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	BLUE (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE (01)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
BLUE		ļ			 			ļ			 						• • • • • • •		
	BLUE (62)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	BLUE (63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	 1	1	1



3-7. Power Sequence

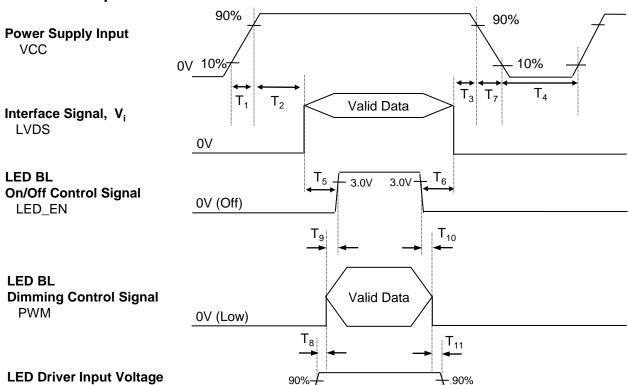


Table 6. POWER SEQUENCE TABLE

 T_{13}

10%

Logic		Value		Linita	LED		Value		Lloito
Parameter	Min.	Тур.	Max.	Units	Parameter	Min.	Тур.	Max.	Units
T ₁	0.5		10	ms	T ₈	10	-	-	ms
T ₂	0		50	ms	T ₉	0	-	-	ms
T ₃	0	-	50	ms	T ₁₀	0	-	-	ms
T ₄	400	-	-	ms	T ₁₁	10	-	-	ms
T ₅	200	-	-	ms	T ₁₂	0.5	-	-	ms
T ₆	200	-	-	ms	T ₁₃	0	-	5000	ms
T ₇	3	-	10	ms					

Note)

VLED

- 1. Do not insert the mating cable when system turn on.
- 2. Valid Data have to meet "3-3. LVDS Signal Timing Specifications"

0V

- 3. LVDS, LED_EN and PWM need to be on pull-down condition on invalid status.
- 4. LGD recommend the rising sequence of VLED after the Vcc and valid status of LVDS turn on.



4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 20 minutes in a dark environment at 25°C. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of Φ and Θ equal to Θ .

FIG. 1 presents additional information concerning the measurement equipment and method.

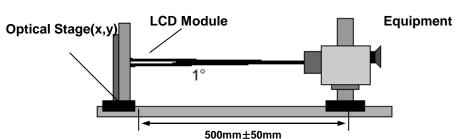


FIG. 1 Optical Characteristic Measurement Equipment and Method

Table 7. OPTICAL CHARACTERISTICS

Ta=25°C, VCC=3.3V, fv=60Hz, f_{CLK}= 70MHz

_			Values			
Parameter	Symbol	Min	Тур	Max	Units	Notes
Contrast Ratio	CR	-	800	.		1
Surface Luminance, white	L _{WH}	255	300	345	cd/m ²	2
Luminance Variation(17P)	δ _{WHITE}	-	1.4	1.6		3
Response Time (G to G)	Tr _{R+} Tr _D	-	20	30	ms	4
Color Coordinates						
RED	RX	0.600	0.630	0.660		
	RY	0.320	0.350	0.380		
GREEN	GX	0.310	0.340	0.370	[
	GY	0.590	0.620	0.650		
BLUE	ВХ	0.125	0.155	0.185		
	BY	0.085	0.115	0.145	[
WHITE	WX	0.283	0.313	0.343	[
	WY	0.299	0.329	0.359		
Viewing Angle						5
x axis, right(Φ=0°)	Θr	80	-	-	degree	
x axis, left (Φ=180°)	Θl	80	-		degree	
y axis, up (Φ=90°)	Θu	80	-		degree	
y axis, down (Φ=270°)	Θd	80	-	-	degree	
Gray Scale					 	6
Flicker		-	-	-30	dB	Half gray (31step)
Color Gamut (1931 NTSC)	C/G	-	60	-	%	



Note)

1. Contrast Ratio(CR) is defined mathematically as

Contrast Ratio =

Surface Luminance with all black pixels

2. Surface luminance is the average of 5 point across the LCD surface 50cm from the surface with all pixels displaying white. For more information see FIG 1.

$$L_{WH} = Average(L_1, L_2, L_4, L_7, L_9)$$

Process control of minimum luminance management is based on 3.98σ level.

3. The variation in surface luminance , The panel total variation (δ_{WHITE}) is determined by measuring L_N at each test position 1 through 13 and then defined as followed numerical formula. For more information see FIG 2.

$$\delta_{\text{WHITE}} = \frac{\text{Maximum}(\textbf{L}_{1}, \textbf{L}_{2}, \ \dots \ \textbf{L}_{15})}{\text{Minimum}(\textbf{L}_{1}, \textbf{L}_{2}, \ \dots \ \textbf{L}_{15})}$$

- 4. Response time is the time required for the display to transition from white to black (rise time, Tr_R) and from black to white(Decay Time, Tr_D). For additional information see FIG 3.
- 5. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG 4.
- 6. Gray scale specification

*
$$f_V = 60Hz$$

Gray Level	Luminance [%] (Typ)
L0	0.1
L7	0.58
L15	4.39
L23	11
L31	20.53
L39	34.98
L47	53.06
L55	73.49
L63	100



FIG. 2 Luminance

<Measuring point for Average Luminance & measuring point for Luminance variation>

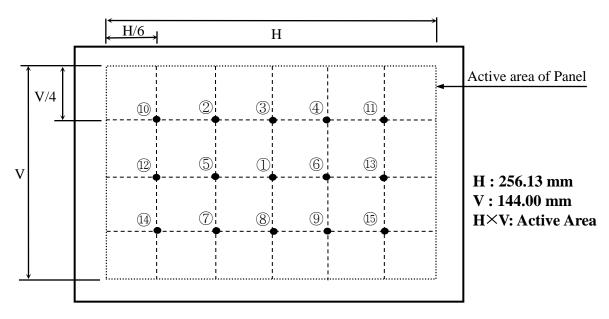
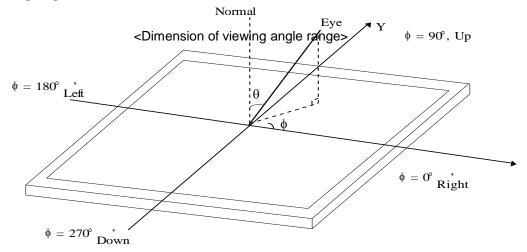


FIG. 3 Response Time

The response time is defined as the following figure and shall be measured by switching the input signal for "gray" and "gray".

FIG. 4 Viewing angle





5. Mechanical Characteristics

The contents provide general mechanical characteristics for the model LP116WH6. In addition the figures in the next page are detailed mechanical drawing of the LCD.

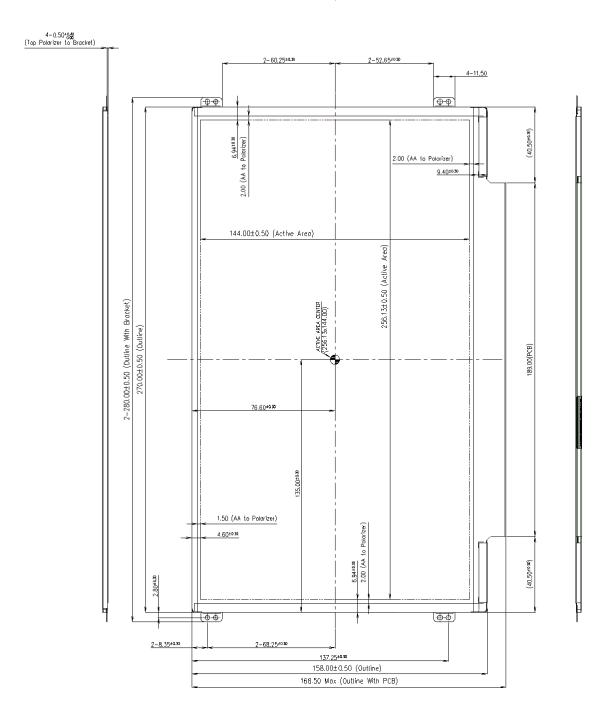
	Horizontal	270.0 ± 0.5mm			
Outline Dimension	Vertical	158.0 ± 0.5mm			
	Thickness	3.1mm (max, W/O PCB Area)			
Bezel Area	Horizontal	260.15 ± 0.5mm			
(Pol. Size)	Vertical	147.50 ± 0.5mm			
Active Diepley Area	Horizontal	256.13mm			
Active Display Area	Vertical	144.00mm			
Weight	200g (Max.)				
Surface Treatment	Hard Coating(3H), Glare treatment of the front polarizer				



<FRONT VIEW>

Note) Unit:[mm], General tolerance: ± 0.5mm

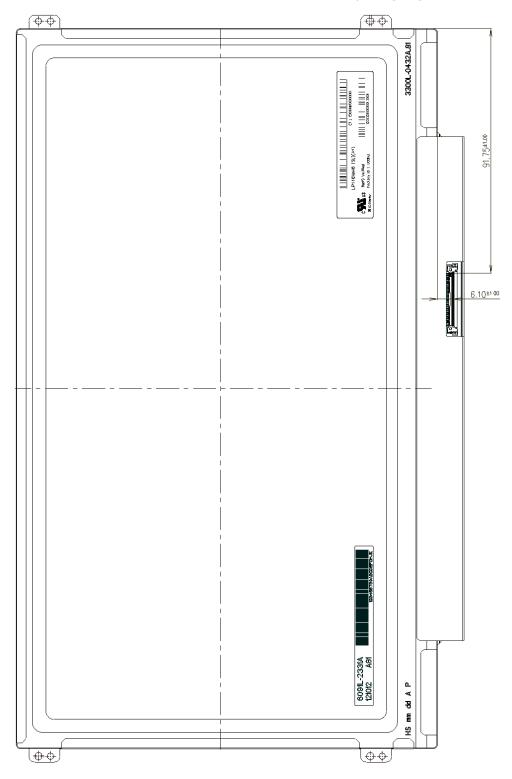






<REAR VIEW>

Note) Unit:[mm], General tolerance: ± 0.5mm





6. Reliability

Environment test condition

No.	Test Item	Conditions
1	High temperature storage test	Ta= 60°C, 240h
2	Low temperature storage test	Ta= -20°C, 240h
3	High temperature operation test	Ta= 50°C, 50%RH, 240h
4	Low temperature operation test	Ta= 0°C, 240h
5	Vibration test (non-operating)	Sine wave, 10 ~ 500 ~ 10Hz, 1.5G, 0.37oct/min 3 axis, 1hour/axis
6	Shock test (non-operating)	Half sine wave, 180G, 2ms one shock of each six faces(I.e. run 180G 2ms for all six faces)
7	Altitude operating storage / shipment	0 ~ 10,000 feet (3,048m) 24Hr 0 ~ 40,000 feet (12,192m) 24Hr

{ Result Evaluation Criteria }

There should be no change which might affect the practical display function when the display quality test is conducted under normal operating condition.

Note: LCM module inspection method & criteria is following IIS spec.



7. International Standards

7-1. Safety

- a) UL 60950-1, Underwriters Laboratories Inc.
 Information Technology Equipment Safety Part 1 : General Requirements.
- b) CAN/CSA-C22.2 No. 60950-1-07, Canadian Standards Association.
 Information Technology Equipment Safety Part 1 : General Requirements.
- c) EN 60950-1, European Committee for Electrotechnical Standardization (CENELEC). Information Technology Equipment Safety Part 1 : General Requirements.
- d) IEC 60950-1, The International Electrotechnical Commission (IEC).
 Information Technology Equipment Safety Part 1 : General Requirements

7-2. Environment

a) RoHS, Directive 2011/65/EU of the European Parliament and of the council of 8 June 2011



8. Packing

8-1. Designation of Lot Mark

a) Lot Mark

А	В	С	D	E	F	G	Н	I	J	К	L	М	
---	---	---	---	---	---	---	---	---	---	---	---	---	--

A,B,C: SIZE(INCH) D: YEAR

E: MONTH $F \sim M$: SERIAL NO.

Note

1. YEAR

Year	2011	2012	2013	2014	2015	2016	2017	2018	2019	2020
Mark	Α	В	С	D	Е	F	G	Н	J	K

2. MONTH

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Mark	1	2	3	4	5	6	7	8	9	Α	В	С

b) Location of Lot Mark

Serial No. is printed on the label. The label is attached to the backside of the LCD module. This is subject to change without prior notice.

8-2. Packing Form

a) Package quantity in one box: 30 (ea)

b) Box Size: 478 * 365 * 244 (mm)



9. PRECAUTIONS

to the polarizer.)

Please pay attention to the followings when you use this TFT LCD module.

9-1. MOUNTING PRECAUTIONS

- (1) You must mount a module using holes arranged in four corners or four sides.
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment.
 Do not touch the surface of polarizer for bare hand or greasy cloth. (Some cosmetics are detrimental
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

9-2. OPERATING PRECAUTIONS

- (1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage : $V=\pm 200 mV$ (Over and under shoot voltage)
- (2) Response time depends on the temperature.(In lower temperature, it becomes longer.)
- (3) Brightness depends on the temperature. (In lower temperature, it becomes lower.)

 And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer.
- (4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.



9-3. ELECTROSTATIC DISCHARGE CONTROL

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

9-4. PRECAUTIONS FOR STRONG LIGHT EXPOSURE

Strong light exposure causes degradation of polarizer and color filter.

9-5. STORAGE

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object. It is recommended that they be stored in the container in which they were shipped.

9-6. HANDLING PRECAUTIONS FOR PROTECTION FILM

- (1) When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
- (2) The protection film is attached to the polarizer with a small amount of glue. If some stress is applied to rub the protection film against the polarizer during the time you peel off the film, the glue is apt to remain on the polarizer.
 - Please carefully peel off the protection film without rubbing it against the polarizer.
- (3) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the polarizer after the protection film is peeled off.
- (4) You can remove the glue easily. When the glue remains on the polarizer surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.



APPENDIX A. Enhanced Extended Display Identification Data (EEDID™) 1/3

	Byte	Byte	Field Name and Comments	Value	Value
	(Dec)	(Hex)	Header	(Hex)	(Bin) 00000000
Header	0	01	Header Header	FF	11111111
	2	02	Header	FF	11111111
	3	03	Header	FF	111111111
	4	04	Header	FF	11111111
	5	05	Header	FF	11111111
	6	06	Header	FF	11111111
	7	07	Header	00	00000000
	8	08	ID Manufacture Name LGD	30	00110000
	9	09	ID Manufacture Name	E4	11100100
 3	10	0A	ID Product Code 0000h	00	00000000
Vendor / Product EDID Version	11 12	0B 0C	(Hex. LSB first)	00	00000000
2 2	13	0D	ID Serial No Optional ("00h" If not used, Number Only and LSB First) ID Serial No Optional ("00h" If not used, Number Only and LSB First)	00	00000000
2 9	14	0E	ID Serial No Optional ("00h" If not used, Number Only and LSB First)	00	00000000
£ 8	15	0F	ID Serial No Optional ("00h" If not used, Number Only and LSB First)	00	00000000
<u> </u>	16	10	Week of Manufacture - Optinal 00 weeks	00	00000000
E E	17	11	Year of Manufacture 2013 years	17	00010111
_	18	12	EDID structure version #= 1	01	00000001
	19	13	EDID revision #= 4	04	00000100
			Video input Definition = Input is a Digital Video signal Interface, Colo Bit Depth: 6 Bits per Primary Color, Digital		
	20	14	Video Interface Standard Supported: Digital Interface is not defined	90	10010000
6	21	15	Horizontal Screen Size (Rounded cm) = 26 cm	1A	00011010
<u> </u>	22	16	Vertical Screen Size (Rounded cm) = 14 cm	0E	00001110
l de su	23	17	Display Transfer Characteristic (Gamma) = (gamma*100)-100 = Example:(2.2*100)-100=120 = 2.2 Gamma	78	01111000
Display Parameters			Feature Support [Display Power Management(DPM) : Standby Mode is not supported, Suspend Mode is not		
Ь	24	18	supported, Active Off = Very Low Power is not supported, Supported Color Encoding Formats: RGB 4:44, Other	02	00000010
			Feature Support Flags: No_sRGB, Preferred Timing Mode, No_Display is continuous frequency (Multi-mode_Base EDID and Extension Block).]		
	25	19	Red/Green Low Bits (RxRy/GxGy)	63	01100011
	26	1A	Blue/White Low Bits (BxBy/WxWy)	E5	11100101
	27	1B	Red X Rx = 0.630	Al	10100001
8 8	28	1C	Red Y Ry = 0.350	59	01011001
Panel Color Coordinates	29	1D	Green X Gx = 0.340	57	01010111
di di	30	1E	Green Y Gy = 0.620	9E	10011110
ne vo	31	1F	Blue X Bx = 0.155	27	00100111
2 Z	32	20	Blue Y By = 0.115	1D	00011101
	33	21	White X Wx = 0.313	50	01010000
	34	22	White Y Wy = 0.329	54	01010100
	34	22	winte 1 wy = 0.329	34	01010100
is he	35	23	Established timing 1 (Optional_00h if not used)	00	00000000
Establishe d d Timings	36	24	Established timing 2 (Optional_00h if not used)	00	00000000
sta Tin	27		1. A	00	00000000
<u>E</u>	37	25	Manufacturer's timings (Optional_00h if not used)	00	00000000
	38	26	Standard timing ID1 (Optional_01h if not used)	01	00000001
	39 40	27 28	Standard timing ID1 (Optional_01h if not used)	01 01	00000001 00000001
	40	29	Standard timing ID2 (Optional_01h if not used) Standard timing ID2 (Optional_01h if not used)	01	00000001
8	42	2A	Standard timing ID3 (Optional 01h if not used)	01	00000001
e I	43	2B	Standard timing ID3 (Optional_Oth if not used)	01	00000001
i,	44	2C	Standard timing ID4 (Optional_01h if not used)	01	00000001
Standard Timing ID	45	2D	Standard timing ID4 (Optional_01h if not used)	01	00000001
	46	2E	Standard timing ID5 (Optional_01h if not used)	01	00000001
	47	2F	Standard timing ID5 (Optional_01h if not used)	01	00000001
	48	30	Standard timing ID6 (Optional_01h if not used)	01	00000001
	49	31 32	Standard timing ID6 (Optional_01h if not used)	01 01	00000001
	50 51	33	Standard timing ID7 (Optional_01h if not used) Standard timing ID7 (Optional_01h if not used)	01	00000001 00000001
	52	34	Standard timing ID8 (Optional_01h ir not used)	01	00000001
	53	35	Standard timing ID8 (Optional 01h if not used)	01	00000001
				VI.	



APPENDIX A. Enhanced Extended Display Identification Data (EEDID™) 2/3

	Byte (Dec)	Byte (Hex)	Field Name and Comments	Value (Hex)	Value (Bin)
Timing Descriptor #1	54	36	Pixel Clock/10,000 (LSB) 70 MHz @ 60Hz	58	01011000
	55	37	Pixel Clock/10,000 (MSB)	1B	00011011
	56	38	Horizontal Active (lower 8 bits) 1366 Pixels	56	01010110
	57	39	Horizontal Blanking(Thp-HA) (lower 8 bits) 126 Pixels	7E	01111110
	58	3A	Horizontal Active / Horizontal Blanking(Thp-HA) (upper 4:4bits)	50	01010000
	59	3B	Vertical Avtive 768 Lines	00	00000000
	60	3C	Vertical Blanking (Tvp-HA) (DE Blanking typ for DE only panels) 14 Lines	0E	00001110
	61	3D	Vertical Active : Vertical Blanking (Tvp-HA) (upper 4:4bits)	30	00110000
	62	3E	Horizontal Sync. Offset (Thfp) 36 Pixels	24	00100100
	63	3F	Horizontal Sync Pulse Width (HSPW) 48 Pixels	30	00110000
	64	40	Vertical Sync Offset(Tvfp): Sync Width (VSPW) 3 Lines: 5 Lines	35	00110101
l Bu	65	41	Horizontal Vertical Sync Offset/Width (upper 2bits)	00	00000000
l m	66	42	Horizontal Image Size (mm) 256 mm	00	00000000
1	67	43	Vertical Image Size (mm) 144 mm	90	10010000
	68	44	Horizontal Image Size / Vertical Image Size	10	00010000
	69	45	Horizontal Border = 0 (Zero for Notebook LCD)	00	00000000
	70	46	Vertical Border = 0 (Zero for Notebook LCD)	00	00000000
	71	47	Non-Interlace, Normal display, no stereo, Digital Separate [Vsync_NEG, Hsync_NEG (outside of V-sync)]	19	00011001
	72	48	Flag	00	00000000
	73	49	Flag	00	00000000
	74	4A	Flag	00	00000000
	75	4B	Data Type Tag (Descriptor Defined by manufacturer)	00	00000000
	76	4C	Flag	00	00000000
63	77	4D	Descriptor Defined by manufacturer	00	00000000
##	78	4E	Descriptor Defined by manufacturer	00	00000000
<u>\$</u>	79	4F	Descriptor Defined by manufacturer	00	00000000
ni.	80	50	Descriptor Defined by manufacturer	00	00000000
essc	81	51	Descriptor Defined by manufacturer	00	00000000
Ą	82	52	Descriptor Defined by manufacturer	00	00000000
Timing Descriptor #2	83	53	Descriptor Defined by manufacturer	00	00000000
imi	84	54	Descriptor Defined by manufacturer	00	00000000
	85	55	Descriptor Defined by manufacturer	00	00000000
	86	56	Descriptor Defined by manufacturer	00	00000000
	87	57	Descriptor Defined by manufacturer	00	00000000
	88	58	Descriptor Defined by manufacturer	00	00000000
	89	59	Descriptor Defined by manufacturer	00	00000000
	90	5A	Flag	00	00000000
	91	5B	Flag	00	00000000
	92	5C	Flag	00	00000000
	93	5D	Data Type Tag (Alphanumeric Data String (ASCII String))	FE	11111110
	94	5E	Flag	00	00000000
5	95	5F	Alphanumeric Data String (ASCII String)	4C	01001100
	96	60	Alphanumeric Data String (ASCII String) G	47	01000111
Timing Descriptor#	97	61	Alphanumeric Data String (ASCII String)	20	00100000
<u> </u>	98	62	Alphanumeric Data String (ASCII String) D	44	01000100
ess	99	63	Alphanumeric Data String (ASCII String) i	69	01101001
l G	100	64	Alphanumeric Data String (ASCII String) s	73	01110011
ing	101	65	Alphanumeric Data String (ASCII String) p	70	01110000
imi	102	66	Alphanumeric Data String (ASCII String)	6C	01101100
Ti.	103	67	Alphanumeric Data String (ASCII String) a	61	01100001
	104	68	Alphanumeric Data String (ASCII String) y	79	01111001
	105	69	Manufacturer P/N(If<13 char> 0Ah, then terminate with ASC II code 0Ah, set remaining char = 20h)	0A	00001010
	106	6A	Manufacturer P/N(If<13 char-> 0Ah, then terminate with ASC II code 0Ah, set remaining char = 20h)	20	00100000
	107	6B	Manufacturer P/N(If<13 char> 0Ah, then terminate with ASC II code 0Ah, set remaining char = 20h)	20	00100000
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APPENDIX A. Enhanced Extended Display Identification Data (EEDID™) 3/3

	Byte (Dec)	Byte (Hex)	Field Name and Comments	Value (Hex)	Value (Bin)
	108	6C	Flag	00	00000000
	109	6D	Flag	00	00000000
	110	6E	Flag	00	00000000
	111	6F	Data Type Tag (Alphanumeric Data String (ASCII String))	FE	11111110
	112	70	Flag	00	00000000
4	113	71	Alphanumeric Data String (ASCII String)	4C	01001100
#: 	114	72	Alphanumeric Data String (ASCII String)	50	01010000
740	115	73	Alphanumeric Data String (ASCII String) 1	31	00110001
, <u>†</u>	116	74	Alphanumeric Data String (ASCII String) 1	31	00110001
3	117	75	Alphanumeric Data String (ASCII String) 6	36	00110110
Timing Descriptor #4	118	76	Alphanumeric Data String (ASCII String) W	57	01010111
	119	77	Alphanumeric Data String (ASCII String)	48	01001000
ii.	120	78	Alphanumeric Data String (ASCII String) 6	36	00110110
I	121	79	Alphanumeric Data String (ASCII String)	2D	00101101
	122	7A	Alphanumeric Data String (ASCII String)	53	01010011
	123	7B	Alphanumeric Data String (ASCII String)	4C	01001100
	124	7C	Alphanumeric Data String (ASCII String)	41	01000001
	125	7 D	Alphanumeric Data String (ASCII String)	31	00110001
Checksum	126	7E	Extension flag (# of optional 128 panel ID extension block to follow, Typ = 0)	00	00000000
	127	7F	Check Sum (The 1-byte sum of all 128 bytes in this panel ID block shall = 0)	48	01001000

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