

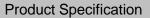


Datasheet

LG Display

HD-10-137

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SPECIFICATION FOR APPROVAL

()	Preliminary Specification
(•	•)	Final Specification

Title	12.5" FHD TFT LCD			
2 1	CHIDDLIED LC Display Co. Ltd			

Customer	
MODEL	

SUPPLIER	LG Display Co., Ltd.		
*MODEL	LP125WF2		
Suffix	SPB2		

^{*}When you obtain standard approval, please use the above model name without suffix

APPROVED BY	SIGNATURE
1	
/	

Please return 1 copy for your confirmation with

your signature and comments.

APPROVED BY	SIGNATURE
REVIEWED BY	
PREPARED BY	
Products Engineeri LG Display Co.,	

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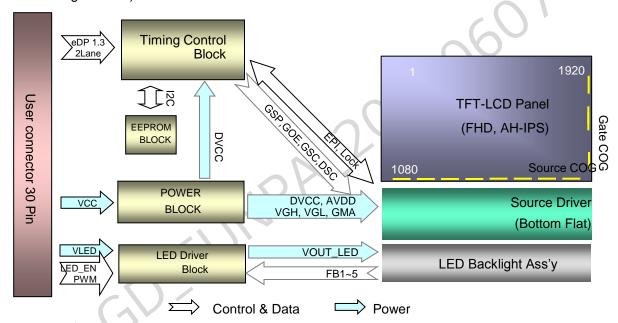
RECORD OF REVISIONS

Revision No	Revision Date	Page	Description	EDID ver
1.0	Oct. 02. 2013	-	First Draft	1.0
1.1	Dec. 30. 2013	22	Update Label Description (Lenovo Code)	
			λ \vee	
			-	



1. General Description

The LP125WF2 is a Color Active Matrix Liquid Crystal Display with an integral LED backlight system and Touch Screen Panel. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive type display operating in the normally black mode. This TFT-LCD has 12.5 inches diagonally measured active display area with Full HD resolution (1920 horizontal by 1080 vertical pixel array). Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 6-bit gray scale signal for each dot, thus, presenting a palette of more than 262,144 colors. The LP125WF2 has been designed to apply the interface method that enables low power, high speed, low EMI. The LP125WF2 is intended to support applications where thin thickness, low power are critical factors and graphic displays are important. In combination with the vertical arrangement of the sub-pixels, the LP125WF2 characteristics provide an excellent flat display. LP125WF2 is the Touch total solution' model. It means it includes LCM & TSP all. (TSP is assembled by a 'Direct Bonding' method)



General Features

Active Screen Size	12.5 inches diagonal				
Outline Dimension	290.5(H, typ) × 170.7(V, typ) × 2.85(D,max) [mm]				
Pixel Pitch	0.144mm x 0.144mm (176ppi)				
Pixel Format	1920 horiz. By 1080 vert. Pixels RGB strip arrangement				
Color Depth	6-bit, 262,144 colors				
Luminance, White	400 cd/m ²				
Power Consumption	Total 5.0 W(Typ.) Logic : 1.1 W (Typ.@ Mosaic), B/L : 3.9 W (Typ.@ VLED 12V)				
Weight	230g (Max.)				
Display Operating Mode	Transmissive mode, normally black				
Surface Treatment	Anti-Glare treatment of the front Polarizer				
RoHS Compliance	Yes				
BFR/PVC/As Free	Yes for all				



2. Absolute Maximum Ratings

The following are maximum values which, if exceeded, may cause faulty operation or damage to the unit.

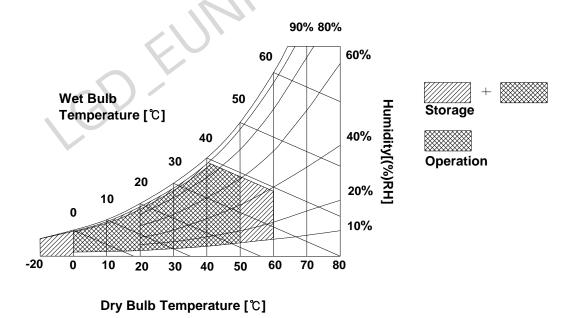
Values Parameter Symbol Units Notes Min Max Power Input Voltage VCC Vdc at $25 \pm 5^{\circ}C$ -0.3 4.0 Operating Temperature TOP 0 50 °C 1, 2 °C Storage Temperature Hst -20 60 1 Operating Ambient Humidity 10 90 %RH 1 Нор Storage Humidity %RH Hst 10 90 1

Table 1. ABSOLUTE MAXIMUM RATINGS

Note: 1. Temperature and relative humidity range are shown in the figure below.

Wet bulb temperature should be 39°C Max, and no condensation of water.

- LCD Surface Temperature Spec : Panel surface temperature should be under 60 degrees operating at 50 degrees / 2hrs
- 3. Storage Condition is guaranteed under packing condition.



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3. Electrical Specifications

3-1. Electrical Characteristics

The LP125WF2 requires two power inputs. The first logic is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The second backlight is the input about LED BL with LED Driver.

Table 2. ELECTRICAL CHARACTERISTICS

Damanast	Comple at		Values		Not		
Paramet	Symbol	Min	Тур	Max	Unit	Notes	
LOGIC :				4	1		
Power Supply Input Vol	tage	Vcc	3.0	3.3	3.6	V	1
	Mosaic	Icc	-	325	375	mA	2
Power Supply Input Current	Red	Icc	-	395	453	mA	2
mpat Garront	Sub-v Line	Icc	-	530	610	mA	2
Power Consumption		Pcc		1.1	1.2	W	2
Power Supply Inrush Co	urrent	Icc_p		-	1500	mA	3
eDP Impedance		ZLVDS	90	100	110	Ω	4
BACKLIGHT : (with LED	Driver)	Or					
LED Power Input Voltag	LED Power Input Voltage			12.0	21.0	V	5
LED Power Input Currer	LED Power Input Current		-	325	335	mA	6
LED Power Consumptio	LED Power Consumption		-	3.9	4.0	W	6
LED Power Inrush Curre	ent	ILED_P	-	-	1500	mA	7
PWM Duty Ratio			1	-	100	%	8
PWM Jitter		-	0	-	0.2	%	9
PWM Impedance		Zpwm	20	40	60	kΩ	
PWM Frequency		Fрwм	200	-	1000	Hz	10
PWM High Level Voltag	V _{PWM_H}	2.2	-	5.3	V		
PWM Low Level Voltage		V _{PWM_L}	0	-	0.6	V	
LED_EN Impedance		Zpwm	20	40	60	kΩ	
LED_EN High Voltage		VLED_EN_H	2.2	-	5.3	V	
LED_EN Low Voltage	LED_EN Low Voltage			-	0.6	V	
Life Time			12,000	-	_	Hrs	11

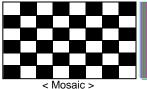
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Note)

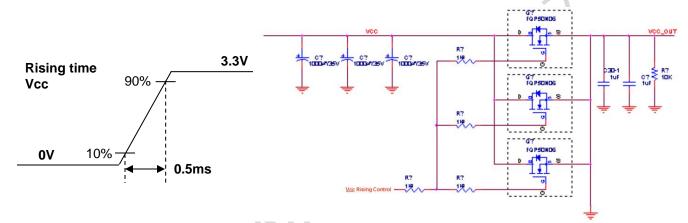
- 1. The measuring position is the connector of LCM and the test conditions are under 25 $^{\circ}$ C, fv = 60Hz, Black pattern.
- 2. The specified lcc current and power consumption are under the Vcc = 3.3V, 25°C, fv = 60Hz condition and Mosaic pattern.
- 2-1. Max Input current (@Sub-V line)

- Vcc 3.0V : 610mA- Vcc 3.3V : 680mA

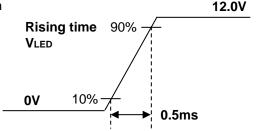




- 3. This Spec. is the max load condition for the cable impedance designing.
- 4. The below figures are the measuring Vcc condition and the Vcc control block LGD used. The Vcc condition is same as the minimum of T1 at Power on sequence.



- 5. This impedance value is needed for proper display and measured form eDP Tx to the mating connector.
- 6. The measuring position is the connector of LCM and the test conditions are under 25 ℃.
- 7. The current and power consumption with LED Driver are under the $V_{LED} = 12.0 \text{V}$, $25 \,^{\circ}\text{C}$, Dimming of Max luminance and White pattern with the normal frame frequency operated(60Hz).
- The below figures are the measuring V_{LED} condition and the V_{LED} control block LGD used.
 V_{LED} control block is same with Vcc control block.



- 9. The operation of LED Driver below minimum dimming ratio may cause flickering or reliability issue.
- 10. If Jitter of PWM is bigger than maximum, it may induce flickering.
- 11. This Spec. is not effective at 100% dimming ratio as an exception because it has DC level equivalent to 0Hz. In spite of acceptable range as defined, the PWM Frequency should be fixed and stable for more consistent brightness control at any specific level desired.
- 12. The life time is determined as the time at which brightness of LCD is 50% compare to that of minimum value specified in table 7. under general user condition.



3-2. Interface Connections

This LCD employs two interface connections, a 40 pin connector used for the module electronics interface and the other connector used for the integral backlight system.

Table 3. MODULE CONNECTOR PIN CONFIGURATION (CN1)

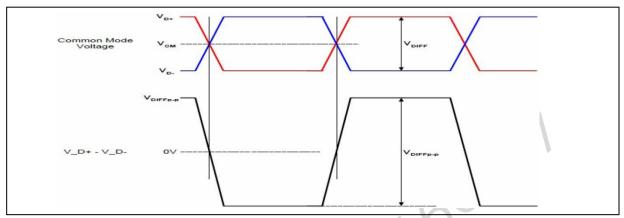
Pin	Symbol	Description	Notes		
1	NC	No Connection (Reserved for LGD)	[Interface Chip]		
2	GND	High Speed (Main Link) Ground	LCD: Parade, DP643 (LCD Controller		
3	ML1-	Complement Signal-Lane 1	Including eDP Receiver.		
4	ML1+	True Signal-Main Lane 1	2. System: TBD or equivalent		
5	GND	High Speed (Main Link) Ground	* Pin to Pin compatible with eDP		
6	ML0-	Complement Signal-Lane 0	00		
7	ML0+	True Signal-Main Lane 0	[Connector]		
8	GND	High Speed (Main Link) Ground	KN38-30S-0.5H, HIROSE		
9	AUX+	True Signal-Auxiliary Channel	[Connector pin arrangement]		
10	AUX-	Complement Signal-Auxiliary Channel			
11	GND	High Speed (Main Link) Ground	30 1 		
12	VCC	VCC for Module (3.3V)			
13	VCC	VCC for Module (3.3V)	[LCD Module Rear View]		
14	NC	No Connection (Reserved for LGD)	[LCD Module Real View]		
15	GND	LCM Ground (Logic Ground)	* Pin for PVcom : #24, #25		
16	GND	LCM Ground (Logic Ground)	PVcom Address : 01010000		
17	HPD	HPD signal pin			
18	GND	LCM Ground (LED Backlight Ground)			
19	GND	LCM Ground (LED Backlight Ground)			
20	GND	LCM Ground (LED Backlight Ground)			
21	GND	LCM Ground (LED Backlight Ground)			
22	LED_EN	LED Backlight On/Off			
23	PWM	System PWM Signal input for dimming			
24	NC	No Connection (Reserved for LGD)			
25	NC	No Connection (Reserved for LGD)			
26	VLED	LED Backlight Power 5V-21V			
27	VLED	LED Backlight Power 5V-21V			
28	VLED	LED Backlight Power 5V-21V			
29	VLED	LED Backlight Power 5V-21V			
30	NC	No Connection (Reserved for LGD)			



3-3. eDP Signal Timing Specifications

3-3-1. DC Specification

The VESA Display Port related AC specification is compliant with the VESA Display Port Standard v1.3



Description	Symbol	Min	Max	Unit	Notes
Differential pools to pools longit voltage		120	7	m\/	For high bit rate
Differential peak-to-peak Input voltage	VDIFF p-p	40	-	mV	For reduced bit rate
Rx DC common mode voltage	Vсм	0	2.0	V	-

3-3-2. AC Specification

The VESA Display Port related AC specification is compliant with the VESA Display Port Standard v1.3

Description	Symbol	Min	Тур	Max	Unit	Notes
Unit Interval for high bit rate (2.7Gbps/lane)	UI_High_Rate	-	370	-	ps	Range is nominal ±350ppm. DisplayPort Link Rx does not require local crystal for link
Unit Interval for high bit rate (1.62Gbps/lane)	UI_Low_Rate	-	617	-	ps	clock generation
Lane-to-Lane skew	V Rx-SKEW- INTER_PAIR	-	-	5200	ps	-
Long intra poir akow	V Rx-SKEW-	•		100	ps	For high bit rate
Lane intra-pair skew	INTRA_PAIR		-	300	ps	For reduced bit rate



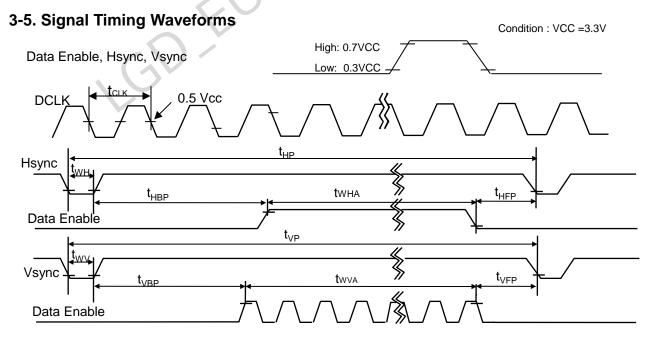
3-4. Signal Timing Specifications

This is the signal timing required at the input of the User connector. All of the interface signal timing should be satisfied with the following specifications and specifications of eDP Tx/Rx for its proper operation.

ITEM Symbol Min Max Unit Note Typ **DCLK** Frequency 138.46 MHz f_{CLK} Period 2090 2106 2122 t_{HP} Hsync Width 28 32 36 tCLK t_{WH} Width-Active 1920 1920 1920 t_{WHA} Period t_{VP} 1090 1095 1100 Vsync Width 3 5 7 tHP t_{WV} Width-Active 1080 1080 1080 t_{WVA} Horizontal back porch 102 106 110 t_{HBP} tCLK Horizontal front porch 40 48 56 t_{HFP} Data Enable 7 Vertical back porch \mathbf{t}_{VBP} 5 9 tHP Vertical front porch 2 3 4 t_{VFP}

Table 4. TIMING TABLE

Appendix) all reliabilities are specified for timing specification based on refresh rate of 60Hz. However, LP125WF2 has a good actual performance even at lower refresh rate (e.g. 40Hz or 50Hz) for power saving mode, whereas LP125WF2 is secured only for function under lower refresh rate. 60Hz at Normal mode, 50Hz, 40Hz at Power save mode. Don't care Flicker level (power save mode).



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3-6. Color Input Data Reference

The brightness of each primary color (red,green and blue) is based on the 6-bit gray scale data input for the color; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

Table 5. COLOR DATA REFERENCE

									Inp	out Co	olor D	ata							
	Color			RI	ΞD					GRE	EEN					BL	UE		
		MSE	3				LSB	_					LSB	MSI					LSB
	Γ	R 5	R 4	R 3	R 2	R 1	R 0	G 5	G 4	G 3	G 2	G 1	G 0	B 5	B 4	В3	B 2	B 1	B 0
	Black	0				0	0	0			0	0	0	0	0			0	0
	Red	1 			1	1	1	0			0	0	0	0				0	0
	Green	0	0			0	0	1				1	1	0	0	0	0	0	0
Basic	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	. 1	.1	1		1
Color	Cyan	0	0	0	0	0	0	1	1	1		1	1	1	1	1	. 1		
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	RED (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (01)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
RED						1													
	RED (62)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (01)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
GREEN																			
	GREEN (62)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	GREEN (63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	BLUE (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
BLUE	BLUE (01)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
		ļ			 												 		
	BLUE (62)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	BLUE (63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1



3-7. Power Sequence

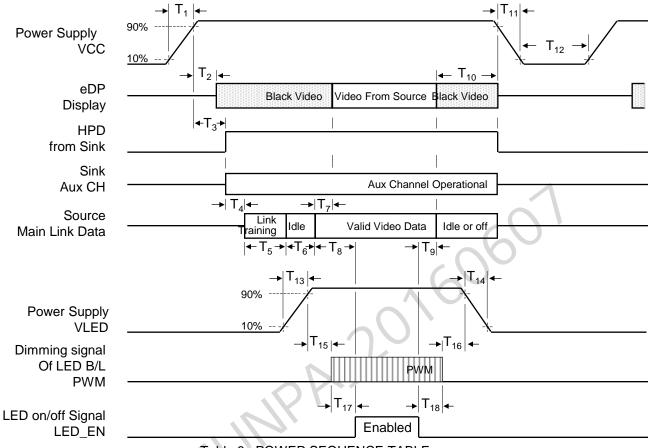


Table 6. POWER SEQUENCE TABLE

Timing	Required	Lin	nits	Units	Notes
Timing	Ву	Min	Max	Units	notes
T ₁	Source	0.5	10	ms	-
T ₂	Sink	0	200	ms	-
T ₃	Sink	0	200	ms	-
T ₄	Source	1	ı	ms	-
T ₅	Source	-	1	ms	-
T ₆	Source	-	1	ms	-
T ₇	Sink	0	50	ms	-
T ₈	Source			ms	LGD recommend Min 200ms
T ₉	Source	-	-	ms	-

Timing	Required	Lir	nits	Units	Notes
Tilling	Ву	Min	Max	Units	Notes
T ₁₀	Source	0	500	ms	-
T ₁₁	Source	1	10	ms	-
T ₁₂	Source	150	ı	ms	VESA recommend Min 500ms
T ₁₃	Source	0.5	10	ms	•
T ₁₄	Source	0	5000	ms	•
T ₁₅	Source	0	ı	ms	1
T ₁₆	Source	0	ı	ms	1
T ₁₇	Source	0	-	ms	-
T ₁₈	Source	0	-	ms	-

Note) 1. Do not insert the mating cable when system turn on.

- 2. Valid Data have to meet "3-3. eDP Signal Timing Specifications"
- 3. Video Signal, LED_EN and PWM need to be on pull-down condition on invalid status.
- 4. LGD recommend the rising sequence of VLED after the Vcc and valid status of Video Signal turn on.



4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 20 minutes in a dark environment at 25°C. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of Φ and Θ equal to 0° .

FIG. 1 presents additional information concerning the measurement equipment and method.

Optical Stage(x,y)

1°

500mm±50mm

FIG. 1 Optical Characteristic Measurement Equipment and Method

Table 7. OPTICAL CHARACTERISTICS

Ta=25°C, VCC=3.3V, fv=60Hz, f_{CLK}= 138.46 MHz

			1a=25°C	, VCC=3.3V	, I∨=60H2	z, f _{CLK} = 138.46 MHz
Parameter	Symbol		Values		Units	Notes
1 didiffeter	Cynnoon	Min	Тур	Max	Office	140103
Contrast Ratio	CR	600	700	l .	L	1
Surface Luminance, white	L _{wH}	320	400	l .	cd/m ²	2
Luminance Variation	$\delta_{\text{WHITE(5p)}}$.	[1.25		3
Lummance variation	$\delta_{\text{WHITE(13p)}}$	-	1.4	1.6	.	
Response Time (W to B)	Tr _{R +} Tr _D	-	35	50	ms	4
Color Coordinates]					
RED	RX	0.574	0.604	0.634		
	RY	0.321	0.351	0.381		
GREEN	GX	0.305	0.335	0.365		
	GY	0.528	0.558	0.588	1	
BLUE	BX	0.118	0.148	0.178	[
	BY	0.083	0.113	0.143		
WHITE	WX	0.283	0.313	0.343		
	WY	0.299	0.329	0.359		
Viewing Angle				[5
x axis, right(Φ=0°)	⊙r	80	-	-	degree	
x axis, left (Φ=180°)	Θl	80	-	[degree	CR ≥ 10
y axis, up (Φ=90°)	Θu	80	-	-	degree	UK 2 IU
y axis, down (Φ=270°)	Θd	80	-		degree	
Gray Scale				.		6
Color Gamut	%	-	50	-		

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Note)

1. Contrast Ratio(CR) is defined mathematically as

2. Surface luminance is the average of 5 point across the LCD surface 50cm from the surface with all pixels displaying white. For more information see FIG 1.

$$LWH = Average(L1,L2, ... L5)$$

The variation in surface luminance, The panel total variation (WHITE) is determined by measuring LN at each test position 1 through 13 and then defined as followed numerical formula.
 For more information see FIG 2.

$$WHITE_5P = \frac{Maximum(L1,L2, ... L5)}{Minimum(L1,L2, ... L5)}$$

$$WHITE_13P = \frac{Maximum(L1,L2, ... L13)}{Minimum(L1,L2, ... L13)}$$

- 4. Response time is the time required for the display to transition from white to black (rise time, TrR) and from black to white(Decay Time, TrD). For additional information see FIG 3.
- 5. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG 4.
- 6. Gray scale specification

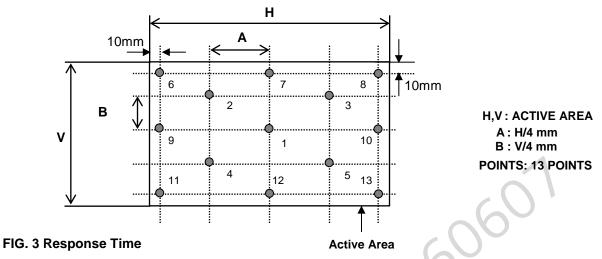
	٢ť	V =	60	Ηz
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Gray Level	Luminance [%] (Typ)
L0	0.16
L31	0.97
L63	4.26
L95	10.5
L127	19.8
L159	33.6
L191	52.1
L223	74.8
L255	100

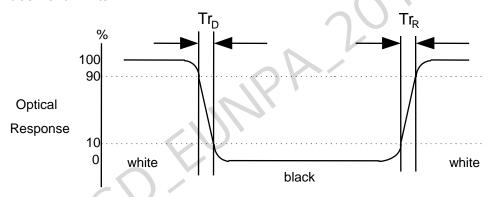


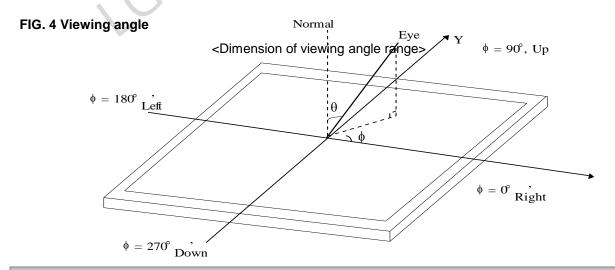
FIG. 2 Luminance

<Measuring point for Average Luminance & measuring point for Luminance variation>



The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".







5. Mechanical Characteristics

The contents provide general mechanical characteristics for the model LP125WF2. In addition the figures in the next page are detailed mechanical drawing of the LCD.

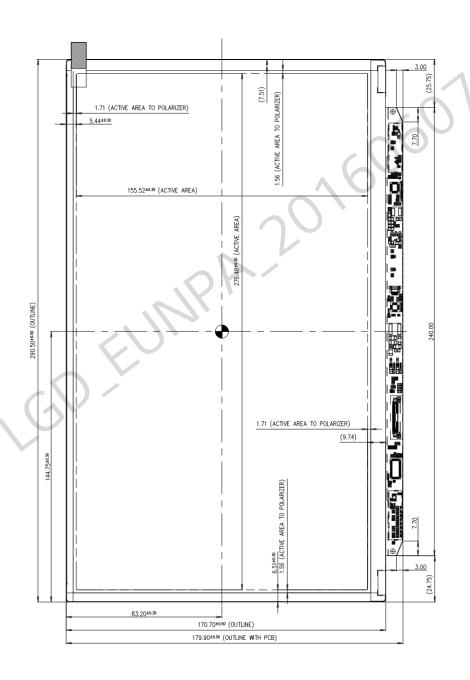
	Horizontal	290.5 ± 0.5mm
Outline Dimension	Vertical	170.7± 0.5mm
	Thickness	2.85mm (max)
Bezel Area	Horizontal	279.60 ± 0.5mm
Dezei Alea	Vertical	158.94 ± 0.5mm
Active Diapley Area	Horizontal	276.48 ± 0.3m
Active Display Area	Vertical	155.52 ± 0.3mm
Weight	230g (Max.)	60
Surface Treatment	Anti-Glare treatment of the front pola	arizer



<FRONT VIEW>

Note) Unit:[mm], General tolerance: ± 0.5mm

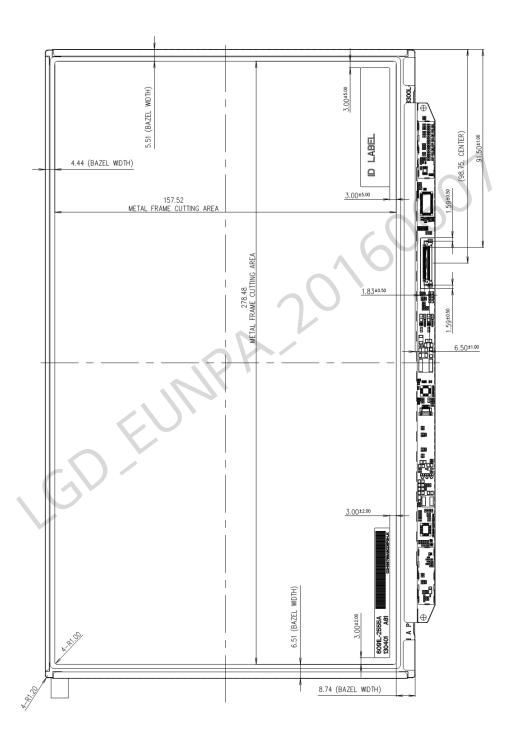






<REAR VIEW>

Note) Unit:[mm], General tolerance: \pm 0.5mm LCD label must not be overlapped with metal frame.





6. Reliability

Environment test condition

No.	Test Item	Conditions
1	High temperature storage test	Ta= 60°C, 240h
2	Low temperature storage test	Ta= -20°C, 240h
3	High temperature operation test	Ta= 50°C, 50%RH, 240h
4	Low temperature operation test	Ta= 0°C, 240h
5	Vibration test (non-operating)	Random, 1.0Grms, 10 ~ 300Hz(PSD 0.0035) 3 axis, 30min/axis
6	Shock test (non-operating)	 No functional or cosmetic defects following a shock to all 6 sides delivering at least 180 G in a half sine pulse no longer than 2 ms to the display module No functional defects following a shock delivering at least 200 g in a half sine pulse no longer than 2 ms to each of 6 sides. Each of the 6 sides will be shock tested with one each display, for a total of 6 displays
7	Altitude operating storage / shipment	0 ~ 10,000 feet (3,048m) 24Hr 0 ~ 40,000 feet (12,192m) 24Hr

[{] Result Evaluation Criteria }

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There should be no change which might affect the practical display function when the display quality test is conducted under normal operating condition.



7. International Standards

7-1. Safety

- a) UL 60950-1, Underwriters Laboratories Inc.
 Information Technology Equipment Safety Part 1 : General Requirements.
- b) CAN/CSA-C22.2 No. 60950-1-07, Canadian Standards Association.
 Information Technology Equipment Safety Part 1 : General Requirements.
- c) EN 60950-1, European Committee for Electrotechnical Standardization (CENELEC). Information Technology Equipment Safety Part 1 : General Requirements.
- d) IEC 60950-1, The International Electrotechnical Commission (IEC).
 Information Technology Equipment Safety Part 1 : General Requirements

7-2. Environment

a) RoHS, Directive 2011/65/EU of the European Parliament and of the council of 8 June 2011

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8. Packing

8-1. Designation of Lot Mark

a) Lot Mark

А	В	С	D	Е	F	G	Н	I	J	К	L	М	
---	---	---	---	---	---	---	---	---	---	---	---	---	--

A,B,C : SIZE(INCH) D : YEAR

E: MONTH $F \sim M$: SERIAL NO.

Note

1. YEAR

Year	2011	2012	2013	2014	2015	2016	2017	2018	2019	2020
Mark	A	В	С	D	E	F	G	H	۲	K

2. MONTH

	Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Г	Mark	1	2	3	4	5	6	7	8	9	Α	В	С

b) Location of Lot Mark

Serial No. is printed on the label. The label is attached to the backside of the LCD module. This is subject to change without prior notice.

8-2. Packing Form

a) Package quantity in one box: 30pcs

b) Box Size: 478mm X 365mm X 244mm

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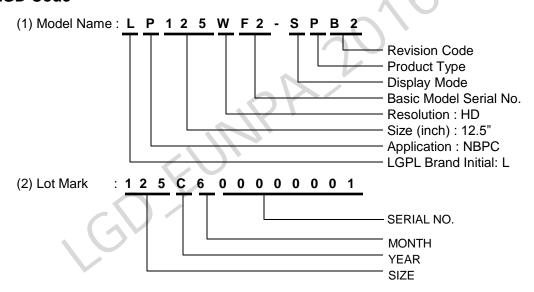


8-3. Label Description

Model Name



LGD Code



Lenovo Code

1)P/N: SD10A09825 2)FRU: 00HM745



9. PRECAUTIONS

Please pay attention to the followings when you use this TFT LCD module.

9-1. MOUNTING PRECAUTIONS

- (1) You must mount a module using holes arranged in four corners or four sides.
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment.
 - Do not touch the surface of polarizer for bare hand or greasy cloth.(Some cosmetics are detrimental to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.
- (10) When handling the LCD module, it needs to handle with care not to give mechanical stress to the PCB and Mounting Hole area."

9-2. OPERATING PRECAUTIONS

- (1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage : $V=\pm\ 200mV(Over\ and\ under\ shoot\ voltage)$
- (2) Response time depends on the temperature.(In lower temperature, it becomes longer.)
- (3) Brightness depends on the temperature. (In lower temperature, it becomes lower.) And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer.
- (4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.

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9-3. ELECTROSTATIC DISCHARGE CONTROL

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

9-4. PRECAUTIONS FOR STRONG LIGHT EXPOSURE

Strong light exposure causes degradation of polarizer and color filter.

9-5. STORAGE

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object. It is recommended that they be stored in the container in which they were shipped.

9-6. HANDLING PRECAUTIONS FOR PROTECTION FILM

- (1) When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
- (2) The protection film is attached to the polarizer with a small amount of glue. If some stress is applied to rub the protection film against the polarizer during the time you peel off the film, the glue is apt to remain on the polarizer.
 - Please carefully peel off the protection film without rubbing it against the polarizer.
- (3) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the polarizer after the protection film is peeled off.
- (4) You can remove the glue easily. When the glue remains on the polarizer surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.

9-7. THE LGD QA RESPONSIBILITY WILL BE AVOIDED IN CASE OF BELOW

- (1) When the customer attaches TSM(Touch Sensor Module) on LCM without Supplier's approval.
- (2) When the customer attaches cover glass on LCM without Supplier's approval.
- (3) When the LCMs were repaired by 3rd party without Supplier's approval.
- (4) When the LCMs were treated like Disassemble and Rework by the Customer and/or Customer's representatives without supplier's approval.

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APPENDIX A. Enhanced Extended Display Identification Data (EEDID™) 1/3

EDID Data for Lenovo _ ver. 1.0

2013.10.08

EDID Data for Lenovo _ ver. 1.0 2013.10					
	Byte (Dec)	Byte (Hex)	Field Name and Comments	Value (Hex)	Value (Bin)
	0	00	Header	00	00000000
	1	01	Header	FF	11111111
	2	02	Header	FF	11111111
Header	3	03	Header	FF	11111111
lea	4	04	Header	FF	11111111
H	5	05	Header	FF	11111111
	6	06	Header	FF	11111111
	7	07	Header	00	00000000
	8	08	ID Manufacture Name LGD	30	00110000
	9	09	ID Manufacture Name	E4	11100100
4.	10	0A	ID Product Code 0437h	37	00110111
nci	11	0B	(Hex. LSB first)	04	00000100
ois.	12	0C	ID Serial No Optional ("00h" If not used, Number Only and LSB First)	00	00000000
'Pı Ver	13	0D	ID Serial No Optional ("00h" If not used, Number Only and LSB First)	00	00000000
) r /	14	0E	ID Serial No Optional ("00h" If not used, Number Only and LSB First)	00	00000000
Vendor / Product EDID Version	15	0F	ID Serial No Optional ("00h" If not used, Number Only and LSB First)	00	00000000
Ve E	16	10	Week of Manufacture - Optinal 00 weeks	00	00000000
	17	11	Year of Manufacture 2013 years	17	00010111
	18	12	EDID structure version # = 1	01	00000001
	19	13	EDID revision # = 4	04	00000100
	20	14	Video input Definition = Input is a Digital Video signal Interface , Colo Bit Depth : 6 Bits per Primary Color , Digital Video Interface Standard Supported: DisplayPort is supported	95	10010101
8	21	15	Horizontal Screen Size (Rounded cm) = 28 cm	1C	00011100
ay ete	22	16	Vertical Screen Size (Rounded cm) = 16 cm	10	00010000
isp l	23	17	Display Transfer Characteristic (Gamma) = (gamma*100)-100 = Example:(2.2*100)-100=120	78	01111000
Display Parameters	24	18	Feature Support [Display Power Management(DPM): Standby Mode is not supported, Suspend Mode is not supported, Active Off = Very Low Power is not supported, Supported Color Encoding Formats: RCB 4:44.4 , Other Feature Support Flags: No_sRCB, Preferred Timing Mode, No_Display is continuous frequency (Multi-mode_Base EDID and Extension Block),]	02	00000010
	25	19	Red/Green Low Bits (RxRy/GxGy)	BF	10111111
	26	1A	Blue/White Low Bits (BxBy/WxWy)	05	00000101
	27	1B	Red X Rx = 0.604	9 A	10011010
Panel Color Coordinates	28	1C	Red Y Ry = 0.351	59	01011001
Ci	29	1D	Green X Gx=0.335	55	01010101
nel ora	30	1E	Green Y Gy = 0.558	8E	10001110
Pai Co	31	1F	Blue X Bx = 0.148	26	00100110
	32	20	Blue Y By = 0.113	1D	00011101
	33	21	White X Wx=0.313	50	01010000
	34	22	White Y Wy = 0.329	54	01010100
19 in	35	23	Established timing 1 (Optional_00h if not used)	00	00000000
Estabi ished Timin	36	24	Established timing 2 (Optional_00h if not used)	00	00000000
E i T	37	25	Manufacturer's timings (Optional_00h if not used)	00	00000000
	38	26	Standard timing ID1 (Optional_01h if not used)	01	00000001
	39	27	Standard timing ID1 (Optional_01h if not used)	01	00000001
	40	28	Standard timing ID2 (Optional_01h if not used)	01	00000001
_	41	29	Standard timing ID2 (Optional_01h if not used)	01	00000001
an and	42	2A 2B	Standard timing ID3 (Optional_Olh if not used) Standard timing ID3 (Optional_Olh if not used)	01 01	00000001
Bu	43		Standard timing ID3 (Optional_OII) if not used) Standard timing ID4 (Optional_OII) if not used)		00000001
mi	44	2C 2D	Standard timing ID4 (Optional_OIh if not used) Standard timing ID4 (Optional_OIh if not used)	01	00000001
T.	45 46	2D 2E	Standard timing ID4 (Optional_OIn if not used) Standard timing ID5 (Optional_OIh if not used)	01	00000001
urd	46	2E 2F	Standard timing ID5 (Optional_Oth ir not used) Standard timing ID5 (Optional_Oth ir not used)	01	00000001
Standard Timing ID	48	30	Standard timing ID5 (Optional_OIn if not used) Standard timing ID6 (Optional_OIh if not used)	01	00000001
	48	31	Standard timing ID6 (Optional_Oth it not used) Standard timing ID6 (Optional_Oth if not used)	01	0000001
	50	32	Standard timing ID7 (Optional_Off if not used)	01	0000001
	51	33	Standard timing ID7 (Optional_Off in not used) Standard timing ID7 (Optional_Off in not used)	01	0000001
	52	34	Standard timing ID8 (Optional_01h if not used)	01	0000001
	53	35	Standard timing ID8 (Optional_01h if not used)	01	00000001
			5 - C-17		



APPENDIX A. Enhanced Extended Display Identification Data (EEDID™) 2/3

	Byte (Dec)	Byte (Hex)	Field Name and Comments	Value (Hex)	Value (Bin)
Timing Descriptor #1	54	36	Pixel Clock/10,000 (LSB) 138.46 MHz @ 60 Hz	16	00010110
	55	37	Pixel Clock/10,000 (MSB)	36	00110110
	56	38	Horizontal Active (HA) (lower 8 bits) 1920 pixels	80	10000000
	57	39	Horizontal Blanking (HB) (lower 8 bits) 186 pixels	BA	10111010
	58	3A	Horizontal Active (HA) / Horizontal Blanking (HB) (upper 4:4bits)	70	01110000
	59	3B	Vertical Avtive (VA) 1080 lines	38	00111000
	60	3C	Vertical Blanking (VB) (DE Blanking typ.for DE only panels) 15 lines	0F	00001111
	61	3D	Vertical Active (VA) / Vertical Blanking (VB) (upper 4:4bits)	40	01000000
	62	3E	Horizontal Front Porch in pixels (HF) (lower 8 bits) 48 pixels	30	00110000
	63	3F	Horizontal Sync Pulse Width in pixels (HS) (lower 8 bits) 32 pixels	20	00100000
	64	40	Vertical Front Porch in lines (VF): Vertical Sync Pluse Width in lines (VS) (lower 4 bits) 3 lines: 5 lines	35	00110101
	65	41	Horizontal Front Porch/ Sync Pulse Width/ Vertical Front Porch/ Sync Pulse Width (upper 2bits)	00	00000000
Tü	66	42	Horizontal Vedio Image Size (mm) (lower 8 bits) 276 mm	14	00010100
	67	43	Vertical Vedio Image Size (mm) (lower 8 bits) 156 mm	9C	10011100
	68	44	Horizontal Image Size / Vertical Image Size (upper 4 bits)	10	00010000
	69	45	Horizontal Border = 0 (Zero for Notebook LCD)	00	00000000
	70	46	Vertical Border = 0 (Zero for Notebook LCD)	00	00000000
	71	47	Non-Interlace, Normal display, no stereo, Digital Separate [Vsync_NEG, Hsync_POS (outside of V-sync)]	1A	00011010
	72	48	Flag	00	00000000
	73	49	Flag	00	00000000
	74	4A	Flag	00	00000000
	75	4B	Data Type Tag (Descriptor Defined by manufacturer)	00	00000000
	76	4C	Flag	00	00000000
2	77	4D	Descriptor Defined by manufacturer	00	00000000
7	78	4E	Descriptor Defined by manufacturer	00	00000000
ipt	79	4F	Descriptor Defined by manufacturer	00	00000000
Timing Descriptor #2	80	50	Descriptor Defined by manufacturer	00	00000000
De	81	51	Descriptor Defined by manufacturer	00	00000000
8u	82	52	Descriptor Defined by manufacturer	00	00000000
mi.	83	53	Descriptor Defined by manufacturer	00	00000000
E	84	54	Descriptor Defined by manufacturer	00	00000000
	85	55	Descriptor Defined by manufacturer	00	00000000
	86	56	Descriptor Defined by manufacturer	00	00000000
	87	57	Descriptor Defined by manufacturer	00	00000000
	88	58	Descriptor Defined by manufacturer	00	00000000
	89	59	Descriptor Defined by manufacturer	00	00000000
	90	5A	Flag	00	00000000
	91	5B	Flag	00	00000000
	92	5C	Flag Data Time Too (Aleksanimonia Data String (ASCH String))	00	00000000
	93	5D	Data Type Tag (Alphanumeric Data String (ASCII String))	FE	11111110
	94 95	5E 5F	Flag Alphanumeric Data String (ASCII String) L	00 4C	00000000 01001100
#3	95	60		47	01001100
tor	97	61	Alphanumeric Data String (ASCII String) G Alphanumeric Data String (ASCII String)	20	00100000
iri.	98	62	Alphanumeric Data String (ASCII String) Alphanumeric Data String (ASCII String) D	44	0100000
esc	99	63	Alphanumeric Data String (ASCII String) i	69	01101001
3 D	100	64	Alphanumeric Data String (ASCII String) s	73	01101001
Timing Descriptor	101	65	Alphanumeric Data String (ASCII String) p	70	01110001
	102	66	Alphanumeric Data String (ASCII String)	6C	0110100
	103	67	Alphanumeric Data String (ASCII String) a	61	01100001
	104	68	Alphanumeric Data String (ASCII String) y	79	01111001
	105	69	Manufacturer P/N(If<13 char> 0Ah, then terminate with ASC II code 0Ah, set remaining char = 20h)	0A	00001010
	106	6A	Manufacturer P/N(If<13 char> 0Ah, then terminate with ASC II code 0Ah,set remaining char = 20h)	20	00100000
					00100000
	107	6B	Manufacturer P/N(If<13 char> 0Ah, then terminate with ASC II code 0Ah,set remaining char = 20h)	20	00100000



APPENDIX A. Enhanced Extended Display Identification Data (EEDID™) 3/3

	Byte (Dec)	Byte (Hex)	Field Name and Comments	Value (Hex)	Value (Bin)
Timing Descriptor #4	108	(')	Flag	00	00000000
	109		Flag	00	00000000
	110	6E	Flag	00	00000000
	111	6F	Data Type Tag (Alphanumeric Data String (ASCII String))	FE	11111110
	112	70	Flag	00	00000000
	113	71	Alphanumeric Data String (ASCII String)	4C	01001100
	114	72	Alphanumeric Data String (ASCII String)	50	01010000
	115	73	Alphanumeric Data String (ASCII String) 1	31	00110001
	116	74	Alphanumeric Data String (ASCII String) 2	32	00110010
	117	75	Alphanumeric Data String (ASCII String) 5	35	00110101
	118	76	Alphanumeric Data String (ASCII String) W	57	01010111
	119	77	Alphanumeric Data String (ASCII String) F	46	01000110
	120	78	Alphanumeric Data String (ASCII String) 2	32	00110010
	121	79	Alphanumeric Data String (ASCII String)	2D	00101101
	122	7A	Alphanumeric Data String (ASCII String)	53	01010011
	123	7B	Alphanumeric Data String (ASCII String)	50	01010000
	124	7C	Alphanumeric Data String (ASCII String) B	42	01000010
	125	7D	Alphanumeric Data String (ASCII String) 2	32	00110010
Check	126	7E	Extension flag (# of optional 128 panel ID extension block to follow, Typ = 0)	00	00000000
	127	7 F	Check Sum (The 1-byte sum of all 128 bytes in this panel ID block shall = 0)	DD	11011101

Our company network supports you worldwide with offices in Germany, Great Britain, Turkey and the USA. For more information please contact:



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